# Rosetta microSDHC™ FIPS 140-2 Non-Proprietary Security Policy

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## <span id="page-3-0"></span>**1. Introduction**

This Security Policy specifies the security rules under which the Rosetta microSDHC™ module operates. The Rosetta microSDHC™ module conforms to FIPS 140-2 Security Requirements for Cryptographic Modules.

Included in these rules are those derived from the security requirements of FIPS 140-2 and additionally, those imposed by SPYRUS, Inc. These rules, in total, define the interrelationship between:

- 1. Operators,
- 2. Services, and
- 3. Critical Security Parameters (CSPs).

<span id="page-3-1"></span>The terms "Rosetta microSDHC™", and "module" are synonymous.

### 1.1 Rosetta microSDHC™ Overview

The Rosetta microSDHC™ module is the latest addition to the SPYRUS family of cryptographic module ICs that enable both smart card and USB cryptographic tokens.

The cryptographic module enables security critical capabilities such as operator authentication, message privacy, integrity, authentication, and non-repudiation; and secure storage, all within a tamper-evident protective coating. The cryptographic module communicates with a host computer via the ports/interfaces defined in [Table 2-1.](#page-7-1)

### <span id="page-3-2"></span>1.2 Rosetta microSDHC™ Implementation

The Rosetta microSDHC™ module is implemented as a multiple-chip embedded module as defined by FIPS 140-2.

The cryptographic module is available in a microSD embodiment with product name: Rosetta microSDHC™.

The term "**microSDHC™**" refers to the cryptographic module IC packaged in the microSDHC (micro Secure Digital High Capacity) form factor, defined as the Rosetta microSDHC™ product.

All Interfaces have been tested and are compliant with FIPS 140-2. Product Identification (including unique part number) for the Rosetta microSDHC™ module is shown in the table below:

Table 1-1 Rosetta microSDHC <sup>TM</sup> Product Identification					
<b>Form Factor</b>	<b>Part Number(s)</b>	<b>Capacity</b>	<b>FW Version</b>		
Rosetta microSDHC™	851314011F	4GB	3.0.2		
	851314012F	8GB	3.0.2		
	851314013F	16GB	3.0.2		

**Table 1-1 Rosetta microSDHC™ Product Identification**

Images of the above form factor are shown in [Figure 1](#page-4-2) below.



**Figure 1 Rosetta microSDHC™ with the Rosetta microSDHC™ Form Factor**

## <span id="page-4-2"></span><span id="page-4-0"></span>1.3 Rosetta microSDHC™ Cryptographic Boundary

The Cryptographic Boundary is defined to be the physical perimeter of the Rosetta microSDHC™ and the potting material it is embedded in (see [Figure 2\)](#page-8-1).

No hardware or firmware components that comprise the Rosetta microSDHC™ are excluded from the requirements of FIPS 140-2.

## <span id="page-4-1"></span>1.4 Approved Mode of Operation

The module only operates in an Approved mode of operation.

The Rosetta microSDHC™ Approved mode of operation is comprised of the Rosetta microSDHC™ command set.

Approved mode of operation commands which are successfully completed will return a standard success return code. The Error return codes are dependent upon the cause of the failure. Services available under the Approved mode of operation are detailed in [Table](#page-9-1)  [3-1](#page-9-1) of this Security Policy.

The Rosetta microSDHC™ supports the following FIPS 140-2 Approved algorithms:





Approved ECDSA (Cert. #578). The Digital Signature will provide between 128-bits to 256-bits of equivalent computational resistance to attack depending upon the size of the curves that are used (P-256, P-384, P-521).

Approved RSA (Cert. #1611). The Digital Signature with a 2048 key size will provide 112 bits of equivalent computational resistance to attack.

Approved SP800-56A, Section 5.7.1.2: ECC CDH Primitive (Cert. #419). The key establishment process will provide between 128-bits to 256-bits of equivalent computational resistance to attack depending upon the size of the ECC CDH curves that are used (P-256, P-384, P-521).

Approved KAS ECC (Cert. #52). The key establishment process will provide between 128-bits to 256-bits of equivalent computational resistance to attack depending upon the size of the keys that are used (P-256, P-384, P-521).

Approved KTS (AES Cert. #3115; key establishment methodology provides between 128 and 256 bits of encryption strength).

The following are available as "non-Approved" algorithms but allowed in FIPS mode:



## <span id="page-6-0"></span>1.5 FIPS 140-2 Security Levels

The cryptographic module complies with the requirements for FIPS 140-2 validation to the levels defined i[n Table 1-4.](#page-6-1) The FIPS 140-2 overall rating of the cryptographic module is Level 3.

<span id="page-6-1"></span>

<b>FIPS 140-2 Category</b>	Level
1. Cryptographic Module Specification	З
2. Cryptographic Module Ports and Interfaces	3
Roles, Services, and Authentication 3.	3
4. Finite State Model	3
5. Physical Security	3
<b>Operational Environment</b> 6.	N/A
7. Cryptographic Key Management	3
8. EMI/EMC*	3
9. Self-tests	3
10. Design Assurance	3
11. Mitigation of Other Attacks	N/A
<b>Overall Security Level</b>	3

Table 1-4 **FIPS 140-2 Certification Levels**

\*Note: The cryptographic module conforms to Level 3 EMI/EMC requirements specified by 47 Code of Federal Regulations, Part 15, Subpart B, Class B.

## <span id="page-7-0"></span>**2. Ports and Interfaces**

The Rosetta microSDHC™ form factor has 8 pins as described in the table below:

<span id="page-7-1"></span>



The Rosetta microSDHC™ pinout is shown in the diagram below [\(Figure 2\)](#page-8-1) with the cryptographic boundary indicated.



**Figure 2 Rosetta microSDHC™ form factor pinout and cryptographic boundary**

## <span id="page-8-1"></span><span id="page-8-0"></span>**3. Roles and Services**

The module supports two roles, Crypto-officer and User, and enforces the separation of these roles by restricting the services available to each one.

**Crypto-officer Role**: The Crypto-officer is responsible for initializing the module. Before issuing the module Rosetta microSDHC™ to an end User, the Crypto-officer initializes the module with private keying material and certificate information. The Crypto-officer cannot use private keys loaded on the module. The module validates the Crypto-officer identity before accepting any initialization commands. The Crypto-officer is also referred to as the Site Security Officer (SSO).

**User Role**: The User role is available after the module has been loaded with a User personality. The User can load, generate and use private keys.

The module validates the User identity before access is granted.

### <span id="page-9-0"></span>3.1 Services

The following table [\(Table 3-1\)](#page-9-1) describes the services provided by the module. The User/SSO column denotes the roles that may execute the service.

<span id="page-9-1"></span>



<sup>1</sup> Refer to ISO/IEC 7816-4 for definition of file types and file system



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In addition to the services listed above in [Table 3-1,](#page-9-1) the following non-security relevant services may be executed while the operator is unauthenticated:

- CREATE
- DELETE
- **DIRECTORY**
- EXTEND
- FIPS INFO
- GET UNAUTHENTICATED RANDOM
- GET RESPONSE
- GET SPYCOS VERSION
- GET STATUS
- READ BINARY
- SELECT
- SELF TEST
- UPDATE BINARY

## <span id="page-12-0"></span>**4. Identification and Authentication**

## <span id="page-12-1"></span>4.1 Initialization Overview

The module is initialized at the factory with a Default SSO PASSWORD Phrase. The SSO must change the default value during logon to make the module ready for initialization. During initialization the module allows the execution of only the commands required to complete the initialization process.

Before a User can access or operate the module, the SSO must initialize it with the User PASSWORD Phrase. The SSO is authorized to log on to the module any time after initialization to change parameters. The module allows 10 consecutive failed SSO logon attempts before it zeroizes all key material and initialization values. In the *zeroized* state, the SSO must use the Default SSO PASSWORD Phrase to log on to the module and must reinitialize all module parameters.

A User must log on to a module to access any on-board cryptographic functions. To log on the User must provide the correct User PASSWORD Phrase. The module allows 10 consecutive failed logon attempts before it blocks the stored User Password. User information stored in the module in non-volatile memory remains resident.

#### <span id="page-13-0"></span>4.2 Authentication

The module implements identity-based authentication which is accomplished by PIN or Password<sup>3</sup> entry by the operator. On invocation by the operator, the module waits for authentication of the User or SSO role by entry of a Password Phrase. There is only one User and one SSO Password allowed per module. Multiple User and SSO accounts are not permitted. The authentication password strength available for each supported role is indicated in [Table 4-2](#page-14-4) below.



#### **Table 4-1 Identification and Authentication Roles and Data**

Once a valid PASSWORD Phrase has been accepted the module cryptographic services may be accessed. The CHECK PASSWORD command includes either the User PASSWORD Phrase as a parameter (or) the SSO PASSWORD Phrase as a parameter. If successful, either the User or SSO gains access to the module.

The module stores the number of logon attempts in non-volatile memory. The count is reset after every successful entry of a User PASSWORD Phrase by a User and after every successful entry of the SSO PASSWORD Phrase by the SSO. If the User role fails to logon to the module in 10 consecutive attempts, the module will zeroize the User PASSWORD Phrase, block all of the User Private Keys and Public Keys, block all of the User Key Registers and disallow User access. The module then transitions to a state that is initialized only for the SSO to perform restorative actions. Restorative actions performed by the SSO may include reloading of initialization parameters, unblocking the User PASSWORD Phrase, or zeroization of the module. When the module is powered up after zeroization, it will transition to the Zeroized State, where it will only accept the Default SSO PASSWORD Phrase. After the Default SSO PASSWORD Phrase has been accepted, the module transitions to the Uninitialized State and must be reinitialized, as described in section 6.

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 $3$  The terms PIN and Password and PASSWORD Phrase are used synonymously in this document.

### <span id="page-14-0"></span>4.3 Strength of Authentication

The strength of the authentication mechanism conforms to the following specifications in Table 4-2. The calculations are based on the enforced minimum PASSWORD Phrase size of 6 bytes.

<span id="page-14-4"></span>

<b>Authentication Mechanism</b>	<b>Strength of Mechanism</b>
Single Password-entry attempt / False Acceptance	The probability that a random 6-byte Password-entry
Rate	(using only 93 keyboard characters <sup>4</sup> ) attempt will
	succeed or a false acceptance will occur is 1.5456185
	$\times$ 10 <sup>-12</sup> . The requirement for a single-attempt / false
	acceptance rate of no more than 1 in 1,000,000 (i.e.
	less than a probability of $10^{-6}$ ) is therefore met.
Multiple Password-entry attempts in one minute	There is a maximum bound of 10 successive failed
	authentication attempts before zeroization occurs.
	The probability of a successful attack of multiple
	attempts in a one minute period is no more than
	1.5456185 $\times$ 10 <sup>-11</sup> due to the enforced maximum
	number of logon attempts. This is less than one in
	100,000 (i.e., 1 x10 <sup>-5</sup> ), as required.

**Table 4-2 Strength of Authentication**

#### <span id="page-14-1"></span>**4.3.1 Obscuration of Feedback**

Feedback of authentication data to an operator is obscured during authentication (e.g., no visible display of characters result when entering a password). The PASSWORD Phrase value is input to the CHECK PASSWORD command as a parameter by the calling application. No return code or pointer to a return value that contains the PASSWORD Phrase is provided.

#### <span id="page-14-2"></span>**4.3.2 Non-weakening Effect of Feedback**

Feedback provided to an operator during an attempted authentication shall not weaken the strength of the authentication mechanism. The only feedback provided by the CHECK PASSWORD command is a return code denoting success or failure of the operation. This information in no way affects the probability of success or failure in either single or multiple attacks.

#### <span id="page-14-3"></span>**4.3.3 Generation of Random Numbers**

The GENERATE RANDOM command can be invoked only after authentication of the User. The SP800-90A DRBG algorithm is used for all authenticated RNG calls.

<sup>4</sup> The character set available for PINs is at least all alphanumeric characters (upper and lower cases) and 31 special keyboard characters comprising the set  $\{-1 \otimes # \otimes \% \wedge \otimes * () \pm \{-1\} \}$   $[ \cdot ] \cdot ; \cdot ]'' \leq \{-2 \}$ .

## <span id="page-15-0"></span>**5 Key Management**

### <span id="page-15-1"></span>5.1 CSP Management



## <span id="page-15-2"></span>5.2 Public Key Management Parameters





### <span id="page-15-3"></span>5.3 CSP Access Matrix

The following table [\(Table 5-3\)](#page-16-0) shows the services (see section 3.1) of the Rosetta microSDHC™, the roles (see section 3) capable of performing the service, the CSPs (see section 5.1) that are accessed by the service and the mode of access (see next paragraph) required for each CSP. The following convention is used: If only one of the roles applies to the service, that role appears alone. If both roles may execute the service, then "User, SSO" is indicated. If either one (but not the other) then "User" or "SSO" is

indicated. In the last option it is a matter of organizational policy which of the roles may execute the service.

<span id="page-16-0"></span>Access modes are R (read), W (write) and E (execute). Destruction is represented as a W.

<b>Service</b>	User / SSO	<b>Access Type</b>	<b>CSP Access</b>	
<b>AES UNWRAPKEY</b>	User	R,E	<b>AES Secret Key</b>	
<b>AES WRAPKEY</b>	User	R,E	<b>AES Secret Key</b>	
AUTHENTICATE SECURE	User,	R,W	<b>ECC CDH Private Key</b>	
<b>CHANNEL</b>	SSO	W	Secure Channel Session Key	
		W,E	<b>ECC CDH Shared Secret</b>	
		W,E	<b>KDF State</b>	
<b>BLOCK PIN</b>	User,	E.	<b>User Password Phrase</b>	
	SSO	E	SSO Password Phrase	
<b>CHANGE PASSWORD</b>	User,	W	<b>User Password Phrase</b>	
	SSO	W	SSO Password Phrase	
<b>CHECK PASSWORD</b>	User,	R.	<b>User Password Phrase</b>	
	SSO	R	SSO Password Phrase	
<b>CREATE</b>	User,	N/A	N/A	
	SSO			
<b>DECRYPT</b>	User	$\mathsf{R}$	<b>AES/TDES Secret Key</b>	
<b>DELETE</b>	User,	N/A	N/A	
	SSO			
<b>DIRECTORY</b>	User,	N/A	N/A	
	SSO			
<b>ECC GENERATE KEY</b>	User	W	<b>ECC CDH Private Key</b>	
<b>ECDH COMPUTE SECRET</b>	User	N/A	N/A	
<b>ECDSA SIGN</b>	User	R	<b>ECDSA Private Key</b>	
<b>ECDSA VERIFY</b>	User,	R.	<b>ECDSA Private Key</b>	
	SSO			
<b>ENCRYPT</b>	User	R	<b>AES/TDES Secret Key</b>	
<b>ENVELOPE</b>	User,	R,E	Secure Channel Session Key	
	SSO			
<b>EXTEND</b>	User,	N/A	N/A	
	SSO			
FIPS_INFO	User,	N/A	N/A	
	SSO			
<b>GENERATE HMAC KEY</b>	User	R,E	<b>HMAC Key</b>	
<b>GENERATE IV</b>	User	N/A	N/A	
<b>GENERATE RANDOM</b>	User	R	<b>HASH DRBG Seed</b>	
GENERATE SYMMETRIC	User	W	<b>MEK</b>	
<b>KEY</b>				
<b>GET PUBLIC</b>	User,	N/A	N/A	
	SSO			
<b>GET RESPONSE</b>	User,	N/A	N/A	
	SSO			
<b>GET SPYCOS VERSION</b>	User,	N/A	N/A	
	SSO			
<b>GET STATUS</b>	User,	N/A	N/A	
	SSO			

Table 5-3 **Rosetta microSDHC™ Access Matrix**





## <span id="page-18-0"></span>5.4 Destruction of Keys and CSPs

The module has the ability to destroy all keys and CSPs by a recursive DELETE command. All keys and CSPs are stored in files. The contents of the file(s) being recursively deleted are erased and over written. Should a power-down occur during the execution of the recursive DELETE, the action of zeroization will resume on a subsequent power-on event, ensuring that access to zeroized information is prevented.

## <span id="page-18-1"></span>**6 Setup and Initialization**

The uninitialized module has only a root directory with minimal version and manufacturing information in specific files. There is no information pertaining to the User or SSO or their authentication data, such as Passwords, stored on the uninitialized module as shipped to the customer.

Initialization of the module is accomplished by setting up a security domain by following the procedures below:

- The SSO creates a new application directory on the module;
- The SSO creates a PIN file that is associated with the SSO and User;
- The SSO initializes the PIN files;
- The SSO may optionally set a default Password or set the User PASSWORD Phrase:
	- o If the User PASSWORD Phrase is set by the SSO, the User will not be able to change their Password.
- The SSO uses FIPS INFO command to confirm FIPS mode.

The module is now in FIPS mode and operators may logon with the CHECK PASSWORD command. See section 4.2 for a description of the CHECK PASSWORD process.

## <span id="page-19-0"></span>**7 Physical Security**

The module is packaged to meet FIPS 140-2 Level 3 Security. The chip is packaged with physical security mechanisms that destroy the chip if physical attacks are launched against it. This is achieved using a hard, opaque, tamper-evident coating on the chip.

The module hardness testing was only performed at a single temperature and no assurance is provided for Level 3 hardness conformance at any other temperature.

<b>Form Factor</b>	<b>Physical</b> <b>Security</b> <b>Mechanisms</b>	<b>Recommended Frequency</b> of Inspection/Test	<b>Inspection/Test</b> <b>Guidance</b> <b>Details</b>
Rosetta microSDHC™	Hard, opaque, tamper-evident coating.	As often as feasible, based upon organization security policy.	Inspect the case of the Rosetta microSDHC™ cover for indicators of penetration (e.g. drill holes, cutting), cracking or other damage. If any signs of suspicious activity are observed, return the cryptographic module to SPYRUS.

Table 7-1 **Inspection of Physical Security Mechanisms**

## <span id="page-20-0"></span>**8 Self-Tests**

The module performs both power-on and conditional self-tests. The power-on self-tests run automatically when power is restored to the module, without requiring any actions or inputs from the operator.

The module performs the following power-on self-tests:

- Firmware Integrity Test with 160-bit Error Detection Code
- Cryptographic algorithm known answer tests (KAT) for:
	- Three-key Triple-DES KAT (encrypt)
	- Three-key Triple-DES KAT (decrypt)
	- AES KAT (encrypt)
	- AES KAT (decrypt)
	- ECDSA KAT (sign)
	- ECDSA KAT (verify)
	- ECC CDH (Primitive "Z" Computation) KAT
	- RSA KAT (sign)
	- RSA KAT (verify)
	- HMAC (SHA-1, SHA-256, SHA-512) KAT
	- SP800-90A DRBG KAT

Power cycling allows either the User or SSO to perform any or all of the above tests on demand.

The module performs the following conditional tests:

- ECDSA Pairwise Consistency Test
- ECC CDH Pairwise Consistency Test
- RSA Pairwise Consistency Test
- Continuous test for Approved SP800-90A DRBG
- Continuous test for non-Approved NDRNG

## <span id="page-20-1"></span>**9 Mitigation of Other Attacks**

The module is not claimed to mitigate against any specific attacks.



### Table 9-1 **Mitigation of Other Attacks**

## <span id="page-21-0"></span>**Appendix A: Critical Security Parameters and Public Keys**

The module supports the following CSPs:

- 1. ECDSA Private Key
- Type: X9.62
- Use: The Private Key of the User employed in Elliptic Curve digital signing operations.
- Generation: As per SP800-133 Section 6.1, key generation is performed as per FIPS 186-4 which is an Approved key generation method.
- Establishment: N/A
- Entry: Encrypted with AES-256
- Output: N/A
- Storage: Plaintext; stored in EEPROM
- Key-to-Entity: User
- Zeroization: Actively overwritten during ZEROIZE service

2. ECC CDH Private Key

- Type: SP 800-56A
- Use: Used in ECC CDH key agreement.

- Generation: As per SP800-133 Section 6.2, the random value (K) needed to generate key pairs for the elliptic curve is the output of the SP800-90A DRBG; this is Approved as per SP800-56A.

- Establishment: N/A
- Entry: N/A
- Output: N/A
- Storage: Plaintext; transient in RAM
- Key-to-Entity: User

- Zeroization: Actively overwritten after channel closure; actively overwritten during ZEROIZE service

- 3. Hash DRBG Seed
- Type: SP800-90A
- Use: Used only in generating the initial state of the SP800-90A DRBG
- Generation: Internally generated using the NDRNG
- Establishment: N/A
- Entry: N/A
- Output: N/A
- Storage: N/A
- Key-to-entity: Process
- Zeroization: Actively overwritten during ZEROIZE service
- 4. HMAC Key
- Type: FIPS 198 HMAC Key
- Use: Used to generate HMAC message authentication code
- Generation: As per SP800-133 Section 7.1, key generation is performed as per the "Direct Generation" of Symmetric Keys which is an Approved key generation method.
- Establishment: N/A
- Entry: Encrypted with AES-256
- Output: Encrypted with AES-256
- Storage: Plaintext; stored in key register
- Key-to-entity: User
- Zeroization: Actively overwritten during ZEROIZE service

5. Message Encryption Key (MEK)

- Type: AES 128, 192, 256 ECB/CBC/CTR Secret Key, Three-key Triple-DES ECB/CBC Secret key

- Use: AES Secret Key for User data encryption/decryption and key wrapping. Three-Key Triple-DES Secret Key for User data encryption/decryption only.

- Generation: As per SP800-133 Section 7.1, key generation is performed as per the "Direct Generation" of Symmetric Keys which is an Approved key generation method.

- Establishment: N/A
- Entry: Encrypted with AES-256
- Output: Encrypted with RSA 2048
- Storage: Plaintext; stored in key register
- Key-to-entity: User
- Zeroization: Actively overwritten during ZEROIZE service
- 6. RSA Private Key for Digital Signatures
- Type: FIPS 186-4
- Use: The Private Key of the User employed in RSA digital signing operations
- Generation: As per SP800-133 Section 6.1, key generation is performed as per FIPS
- 186-4 which is an Approved key generation method.
- Establishment: N/A
- Entry: Encrypted with AES-256
- Output: N/A
- Storage: Plaintext; stored in EEPROM
- Key-to-entity: User
- Zeroization: Actively overwritten during ZEROIZE service
- 7. RSA Private Key for Key Establishment
- Type: FIPS 186-4
- Use: The Private Key of the User employed in RSA Key Unwrapping
- Generation: As per SP800-133 Section 6.2, key generation is performed as per FIPS
- 186-4; this is an allowed method as per FIPS 140-2 IG D.9
- Establishment: N/A
- Entry: Encrypted with AES-256
- Output: N/A
- Storage: Plaintext; stored in EEPROM
- Key-to-entity: User
- Zeroization: Actively overwritten during ZEROIZE service

8. Secure Channel Session Key

- Type: AES-256 CBC

- Use: AES-256 CBC key used to encrypt and decrypt data transmitted to the module

- Generation: N/A

- Establishment: ECC CDH key agreement as per SP800-56A; allowed method as per FIPS 140-2 IG D.8 Scenario 1

- Entry: N/A

- Output: N/A

- Storage: Plaintext; Transient in RAM

- Key-to-entity: User

- Zeroization: Actively overwritten after channel closure; actively overwritten during ZEROIZE service

9. SSO Password Phrase

- Type: 6 - 20 byte Password Phrase

- Use: A secret 6 - 20 byte value used for Crypto-officer (SSO) authentication that is externally - created by SSO during initialization

- Generation: N/A

- Establishment: N/A

- Entry: Encrypted with AES-256

- Output: N/A

- Storage: Plaintext; stored in EEPROM

- Zeroization: Actively overwritten when CHECK PASSWORD and CHANGE PASSWORD services are executed by the SSO; actively overwritten during ZEROIZE service

10. User Password Phrase

- Type: 6 - 20 byte Password Phrase

- Use: A secret 6 - 20 byte value used for User authentication that is externally created by SSO during initialization

- Generation: N/A

- Establishment: N/A

- Entry: Encrypted with AES-256

- Output: N/A

- Storage: Plaintext; stored in EEPROM

- Zeroization: Actively overwritten when CHECK PASSWORD and CHANGE PASSWORD services are executed by the User; Actively overwritten during ZEROIZE service

11. ECC CDH Shared Secret

- Type: SP 800-56A

- Use: Used in ECC CDH key agreement.

- Generation: N/A

- Establishment: ECC CDH key agreement as per SP800-56A; allowed method as per FIPS 140-2 IG D.8 Scenario 1

- Entry: N/A
- Output: N/A
- Storage: Plaintext; transient in RAM
- Key-to-Entity: User

- Zeroization: Actively overwritten upon successful completion of SP800-56A; actively overwritten during ZEROIZE service

12. KDF State

- Type: SP 800-56A (SHA-256 Auxiliary Function H)
- Use: Used in ECC CDH key agreement.
- Generation: N/A

- Establishment: ECC CDH key agreement as per SP800-56A; allowed method as per FIPS 140-2 IG D.8 Scenario 1

- Entry: N/A
- Output: N/A
- Storage: Plaintext; transient in RAM
- Key-to-Entity: User

- Zeroization: Actively overwritten upon successful completion of SP800-56A; actively overwritten during ZEROIZE service

The module supports the following public keys:

- 1. ECDSA Public Key:
- Type: X9.62

- Use: The Public Key of the User employed in Elliptic Curve digital signing operations

- Generation: As per SP800-133 Section 6.1, key generation is performed as per FIPS
- 186-4 which is an Approved key generation method
- Establishment: N/A
- Entry: Encrypted with AES-256
- Output: Encrypted with AES-256
- Storage: Encrypted; stored in EEPROM
- Key-to-entity: User

2. RSA Public Key for Digital Signatures

- Type: FIPS 186-4

- Use: The Public Key of the User employed in RSA digital signature verification operations

- Generation: As per SP800-133 Section 6.1, key generation is performed as per FIPS 186-4 which is an Approved key generation method

- Establishment: N/A
- Entry: Encrypted with AES-256
- Output: Encrypted with AES-256
- Storage: Encrypted; stored in EEPROM
- Key-to-entity: User

#### 3. RSA Public Key for Key Establishment

- Type: FIPS 186-4
- Use: The Public Key of the User employed in RSA Key Wrapping

- Generation: As per SP800-133 Section 6.2, key generation is performed as per FIPS 186-4; this is an allowed method as per FIPS 140-2 IG D.9

- Establishment: N/A
- Entry: Encrypted with AES-256
- Output: Encrypted with AES-256
- Storage: Encrypted; stored in EEPROM
- Key-to-entity: User

4. ECC CDH Public Key

- Type: SP 800-56A
- Use: Used in ECC CDH key agreement.

- Generation: As per SP800-133 Section 6.2, the random value (K) needed to generate key pairs for the elliptic curve is the output of the SP800-90A DRBG; this is Approved as per SP800-56A.

- Establishment: N/A
- Entry: N/A
- Output: Plaintext
- Storage: Plaintext; transient in RAM
- Key-to-Entity: User