Samsung OpenSSL Cryptographic Module

FIPS 140-2 Security Policy

Version 1.8

Last Update: 2014-03-14

1. Introduction

This document is a non-proprietary FIPS 140-2 Security Policy for the Samsung OpenSSL Cryptographic Module. It contains a specification of the rules under which the module must operate and describes how this module meets the requirements as specified in FIPS PUB 140-2 (Federal Information Processing Standards Publication 140-2) for a Security Level 1 multi-chip standalone software module.

1.1. Purpose of the Security Policy

There are three major reasons that a security policy is required:

- For FIPS 140-2 validation
- Allows individuals and organizations to determine whether the cryptographic module, as implemented, satisfies the stated security policy
- Describes the capabilities, protection, and access rights provided by the cryptographic module, allowing individuals and organizations to determine whether it will meet their security requirements

1.2. Target Audience

This document is intended to be part of the package of documents that are submitted for FIPS validation. It is intended for the following people:

- Developers working on the release
- FIPS 140-2 testing lab
- Crypto Module Validation Program (CMVP)
- Consumers

2. Cryptographic Module Specification

This document is the non-proprietary security policy for the Samsung OpenSSL Cryptographic Module, and was prepared as part of the requirements for conformance to Federal Information Processing Standard (FIPS) 140-2, Level 1.

The following section describes the module and how it complies with the FIPS 140-2 standard in each of the required areas.

2.1. Description of Module

The Samsung OpenSSL Cryptographic Module is a software only Security Level 1 cryptographic module that provides general-purpose cryptographic services to the applications. The crypto module runs on an ARM processor.

The following table shows the overview of the security level for each of the eleven sections of the validation.

Security Component	Security Level
Cryptographic Module Specification	1
Cryptographic Module Ports and Interfaces	1
Roles, Services and Authentication	1
Finite State Model	1
Physical Security	N/A
Operational Environment	1
Cryptographic Key Management	1
EMI/EMC	3
Self Tests	1
Design Assurance	3
Mitigation of Other Attacks	N/A

Table 1: Security Levels

The module has been tested on the following platforms:

Module/Implementation	Device	O/S & Ver.
Samsung OpenSSL Cryptographic Module (SecOpenSSL2.0.3)	Samsung Galaxy Note II	Android Jelly Bean 4.1
Samsung OpenSSL Cryptographic Module (SecOpenSSL2.0.3)	Samsung Galaxy S4	Android Jelly Bean 4.2

Table 2: Tested Platform

2.2. Description of FIPS Approved and Non-FIPS Approved Mode

By default, upon initialization, the module performs self-tests and enters the “Non-FIPS” mode. Whenever the external application requires “FIPS-Approved” mode, it needs to invoke all the self-tests by calling FIPS_mode_set(FIPS_MODE_ON). Please note that the self-tests invoked before entering FIPS or Non-FIPS mode are described in section 10.1.

The module can be switched between FIPS-Approved and Non-FIPS mode by invoking the API FIPS_mode_set() using the following parameters:

FIPS_MODE_OFF = 0
FIPS_MODE_ON = 1

In FIPS-Approved mode, the module will be initialized with symmetric algorithms, digest algorithms, HMAC, and random generators.

In the Approved mode the module provides the following approved functions:

- AES (CMAC, CCM, GCM, XTS, CBC, ECB, CFB with 8 bits, CFB with 128 bits, OFB)
- SHA-1, SHA-224, SHA-256, SHA-384, SHA-512
- RNG (ANSI X9.31)
- Triple-DES (CMAC, CBC, ECB, CFB with 8 bits, CFB with 64 bits, OFB)
- HMAC (with SHA-1, SHA-224, SHA-256, SHA-384, SHA-512)
- DRBGs (SP 800-90A: HASH DRBG, HMAC DRBG, CTR DRBG)
- RSA (2048 bits key for all services; 1024 bits key for signature verification only)
- DSA (2048 bits key for all services; 1024 bits key for domain parameters verification and signature verification only)
- ECDSA (P-224, 256, 384, 521 for all services; P-192 for public key verification and signature verification only)
- EC Diffie-Hellman (P-224, 256, 384, 521; K-233, 283, 409, 571; B-233, 283, 409, 571)
- RSA Key Transport (2048-4096 bits key)

Please see Table 5, “Services” in Section 4.2 for the CAVP certificate numbers.

The module implements the following Non-Approved functions, which shall not be used in the FIPS 140-2 approved mode of operation. When invoking one of the non-approved ciphers which are still technically callable as listed above, the module implicitly transitions into non-FIPS mode.

- RSA (1024 bits key for key generation and signature generation)
- DSA (1024 bits key for key generation, domain parameters generation and signature generation)
- ECDSA (P-192 for public key generation and signature generation)
- EC Diffie-Hellman (P-192, K-163, B-163)
- DRBG (non-compliant; SP 800-90A: Dual EC DRBG)
- RSA Key Transport (1024 bits key)
- Blowfish
- Triple-DES-CTR (non-compliant)
- AES-CTR (non-compliant)

- MD4
- MD5
- MDC-2
- RC2
- RC4
- RIPEMD-160
- Diffie-Hellman
- md_rand.c (Non Approved RNG)

Caveat 1: For the cryptographic security reason, the SP 800-90A Dual EC DRBG has been moved to non-Approved functions list to only operate in non-FIPS mode even though the implementation is validated with CAVS cert. #299 and #321. The use of Dual EC DRBG will cause the module to enter non-FIPS mode implicitly.

In the Non-FIPS mode, all the above listed approved and non-approved algorithms except the approved RNG ANSI X9.31 are available for use, although MD5 is allowed for use in TLS only. The non-approved RNG ms_rand.c is available only in the Non-FIPS mode for usage.

2.3. Cryptographic Module Boundary

2.3.1. Software Block Diagram

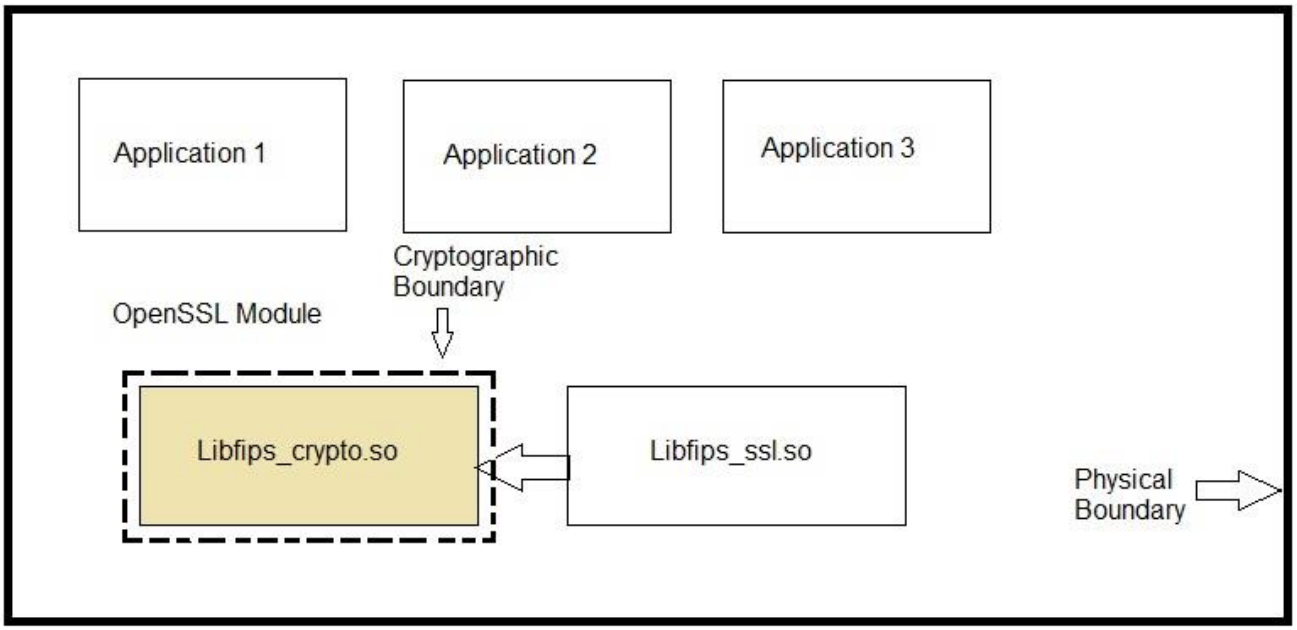

Figure 1: Software Block Diagram

Related documentation:

- S/W Detailed Level Design (FIPS_OpenSSL_Func_Design.docx) version 0.14
- Samsung OpenSSL Cryptographic Module (Samsung_OpenSSLv1.8.doc)

Note: The master component list is provided in Section 6.3 of S/W Detailed Level Design document.

2.3.2. Hardware Block Diagram

This figure illustrates the various data, status and control paths through the cryptographic module. Inside, the physical boundary of the module, the mobile device consists of standard integrated circuits, including processors and memory. These do not include any security-relevant, semi- or custom integrated circuits or other active electronic circuit elements. The physical boundary includes power inputs and outputs, and internal power supplies. The logical boundary of the cryptographic module contains only the security-relevant software elements that comprise the module.

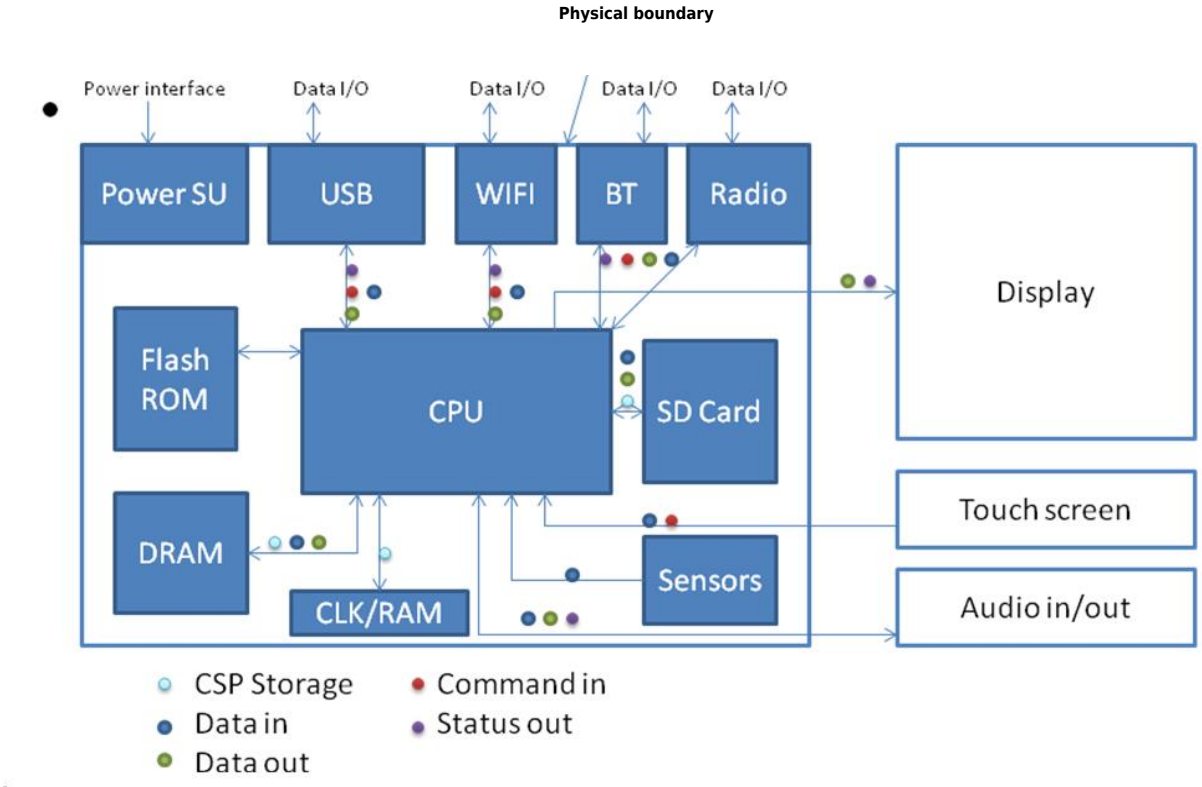

Figure 2: Hardware Block Diagram

3. Cryptographic Module Ports and Interfaces

FIPS Interface	Ports
Data Input	API input parameters
Data Output	API output parameters
Control Input	API function calls
Status Output	API function calls, or configuration files on filesystem
Power Input	Physical power connector

Table 3: Ports and Interfaces

4. Roles, Services and Authentication

4.1. Roles

Role	Services (see list below)
User	Encryption, Decryption, Random Numbers Generation, Digest Creation, Key Generation, Signature Generation, Signature Verification, Key Agreement, Key Transport
Crypto Officer	Configuration, Initialization of Module, Encryption, Decryption, Random Numbers Generation, Digest Creation, Key Generation, Signature Generation, Signature Verification, Key Agreement, Key Transport

Table 4: Roles

The module meets all FIPS 140-2 Level 1 requirements for Roles and Services, implementing both User and Crypto Officer roles. The Module does not allow concurrent operators.

The User and Crypto Officer roles are implicitly assumed by the entity accessing services implemented by the Module. No further authentication is required. The Crypto Officer can initialize the Module.

4.2. Services

Role	Algorithm/Service	CSP	Modes	FIPS Approved (Cert #)	Standard	Access (Read, Write, Execute)
User, Crypto Officer	AES (encryption and decryption)	128, 192, 256 bit keys	ECB, CBC, CFB1, CFB8, CFB128, OFB, CTR	Cert #2351, 2411	FIPS 197	R, W, EX
User, Crypto Officer	XTS with AES (encryption and decryption)	128, 256 bit keys	XTS	Cert#2351, 2411	SP 800-38E	R, W, EX
User, Crypto Officer	GCM with AES (encryption and decryption)	128, 192, 256 bit keys, 96 bit IV supported, MAX IV length 1024	Tag length supports 32, 64, 96, 104, 112, 120, 128	Cert#2351, 2411	SP 800-38D, compliant to section 8.2.1 for IV generation	R, W, EX
User, Crypto Officer	Triple-DES (encryption and decryption)	2 Key and 3 Key	CBC, ECB, OFB, CFB	Cert #1471, 1501	SP 800-67	R, W, EX
User, Crypto Officer	HMAC (with SHA-1, SHA-224, SHA-256, SHA-384, SHA-512) (message digest)	At least 112 bits HMAC Key	N/A	Cert #1458, 1496	FIPS 198	R, W, EX

User, Crypto Officer	SHA-1 SHA-224 SHA-256 SHA-384 SHA-512 (message digest)	N/A	N/A	Cert #2026, 2069	FIPS 180-4	R, W, EX
User, Crypto Officer	CMAC generate and verify with AES	128, 192, 256 bit keys	Supports 0 length, CMAC length supports Min 2, Max 16	Cert#2351, 2411	SP 800-38B	R, W, EX
User, Crypto Officer	CCM generate and verify with AES	128, 192, 256 bit keys	Tag length supports 4, 6, 8, 10, 12, 14, 16	Cert#2351, 2411	SP 800-38C	R, W, EX
User, Crypto Officer	CMAC generate and verify with Triple-DES	3 key Triple-DES	Supports 0 length	Cert #1471, 1501	SP 800-38B	R, W, EX
User, Crypto Officer	DRBG (Random number generation)	Seed	HASH_DRBG, HMAC_DRBG, CTR_DRBG	Cert #299, 321	SP 800-90A	R, W, EX
User, Crypto Officer	ECDSA (public key generation, signature generation)	P-224, 256, 384, 521	N/A	Cert #386, 396	FIPS 186-2, FIPS 186-4	R, W, EX
User, Crypto Officer	ECDSA (public key verification, signature verification)	P-192, 224, 256, 384, 521	N/A	Cert #386, 396	FIPS 186-2, FIPS 186-4	R, W, EX
User, Crypto Officer	KASECC Component Test (key establishment/ agreement)	P-224, 256, 384, 521 K-233, 283, 409, 571 B-233, 283, 409, 571	Section 5.7.1.2 ECC CDH Primitive	Cert #56, 72	SP 800-56A	R, W, EX
User, Crypto Officer	RSA (key generation, signature generation, key transport)	2048 bit keys	N/A	Cert #1212, 1245	FIPS 186-2	R, W. EX

User, Crypto Officer	RSA (signature verification)	1024, 2048 bit keys	N/A	Cert #1212, 1245	FIPS 186-2	R, W. EX
User, Crypto Officer	DSA (key generation, domain parameters generation, signature generation)	2048 bit keys	N/A	Cert #735, 753	FIPS 186-2, FIPS 186-4	R, W, EX
User, Crypto Officer	DSA (domain parameters verification, signature verification)	1024, 2048 bit keys	N/A	Cert #735, 753	FIPS 186-2, FIPS 186-4	R, W, EX
User, Crypto Officer	RNG ANSI X9.31	Seed and Seed Key	AES-128, AES-192, AES-256	Cert #1171, 1190	ANSI X9.31	R, W, EX
Crypto Officer	Initialization	N/A	N/A	N/A	-	N/A
User, Crypto Officer (self test is executed automatically when device is booted or restarted)	Self Test	N/A	N/A	N/A	-	N/A
User, Crypto Officer	Check Status/Get State	N/A	N/A	N/A	-	R
Crypto Officer	Configuration	N/A	N/A	N/A	-	R, W, EX
User, Crypto Officer	Zeroization of Symmetric Keys	AES/Triple-DES Keys	N/A	N/A	-	R, W, EX
User, Crypto Officer	Zeroization of HMAC Keys	HMAC Keys	N/A	N/A	-	R, W, EX
User, Crypto Officer	Zeroization of Asymmetric Keys	RSA/DSA/ ECDSA Keys	N/A	N/A	-	R, W, EX

Table 5: Services

The API document associated with the Security Functions is provided upon request.

Caveat 2: NIST SP 800-131A describes the transition associated with the use of cryptographic algorithms and key lengths. Based on the information included in this publication, the following algorithms implemented in this cryptographic module will become “disallowed” after 2013 or 2015, so their usage is discouraged as they cannot be used in FIPS mode after the transition period:

- DSA Key Generation and Digital Signature Generation with keys of length < 2048 bits
- RSA Key Generation and Digital Signature Generation with keys of length < 2048 bits
- EC Diffie- Hellman's Key Agreement using elliptic curves with keys of length < 224 bits
- RSA Key Wrapping with keys of length < 2048 bits
- RNG specified in ANSI X9.31
- SHA-1 for digital signature generation
- HMAC with key lengths < 112 bits

Caveat 3: Elliptic Curve Diffie-Hellman (ECDH) with 233-571 bits curves (P-224, P-256, P-384, P-521, K-233, K-283, K-409, K-571, B-233, B-283, B-409, B-571) providing 112-256 bits of security strength.

Caveat 4: RSA Encrypt/Decrypt for Key Wrapping with 2048-4096 bits of keys to provide 112-150 bits of security strength.

Caveat 5: The module generates cryptographic keys whose strengths are modified by available entropy.

Caveat 6: In case the Module's power is lost and then restored, the calling application must ensure that the keys used for the AES GCM encryption/decryption are re-distributed.

The following table identifies the non-FIPS-approved services:

Role	Service (Description)	CSP	Access (Read, Write, Execute)
Block Ciphers			
User, Crypto Officer	AES CTR mode (encryption and decryption)	128, 192, 256 bit Symmetric keys	R, W, EX
User, Crypto Officer	Triple-DES CTR mode (encryption and decryption)	2-key and 3-key	R, W, EX
User, Crypto Officer	Blowfish (encryption and decryption)	variable key length from 32 bits up to 448 bits	R, W, EX
User, Crypto Officer	RC2 (encryption and decryption)	Variable key size from 8 bits up to 1024 bits	R, W, EX
User, Crypto Officer	RC4 (encryption and decryption)	Variable key size from 40 bits up to 2048 bits	R, W, EX
Asymmetric Ciphers			
User, Crypto Officer	RSA (key generation)	1024 bits modulus size	R, W, EX
User, Crypto Officer	RSA (signature generation)	1024 bits modulus size, or using SHA-1 for any modulus size	R, W, EX

Role	Service (Description)	CSP	Access (Read, Write, Execute)
User, Crypto Officer	DSA (domain parameter generation, key generation and signature generation)	1024 bits modulus size	R, W, EX
User, Crypto Officer	ECDSA (P-192 for public key generation and signature generation)	Asymmetric key pair	R, W, EX
Message Digest/Message Authentication Code (MAC)			
User, Crypto Officer	MDC-2 (cryptographic hash)	N/A	R, W, EX
User, Crypto Officer	MD4 (cryptographic hash)	N/A	R, W, EX
User, Crypto Officer	MD5 (cryptographic hash)	N/A	R, W, EX
User, Crypto Officer	RIPEMD-160 (cryptographic hash)	N/A	R, W, EX
Key Establishment			
User, Crypto Officer	Diffie-Hellman (non-compliant) (Key Agreement)	Asymmetric key pair, secret key	R, W, EX
User, Crypto Officer	EC Diffie-Hellman (P-192, K-163, B-163; non-compliant) (Key Agreement)	Asymmetric key pair, secret key	R, W, EX
User, Crypto Officer	RSA Key Transport	1024 bits Asymmetric key pair	R, W, EX
Non-approved RNGs (The following RNGs are available only in Non-approved mode)			
User, Crypto Officer	DRBG (non-compliant; SP 800-90A: Dual EC DRBG)	Entropy input string, seed and s	R, W, EX
User, Crypto Officer	md_rand.c (Non Approved RNG)	Seed	R, W, EX

Table 6: Non-Approved Services

4.3. Operator Authentication

There is no operator authentication; assumption of role is implicit by action.

4.4. Mechanism and Strength of Authentication

No authentication is required at security level 1; authentication is implicit by assumption of the role.

5. Finite State Machine

For information pertaining to the Finite State Model, please refer to the Functional Design document.

6. Physical Security

The module is comprised of software only and thus does not claim any physical security.

7. Operational Environment

This module will operate in a modifiable operational environment per the FIPS 140-2 definition. Please refer to Table 2 in section 2.1 for information on tested configuration and the operational environment.

7.1. Policy

The operating system shall be restricted to a single operator mode of operation (i.e., concurrent operators are explicitly excluded).

The external application that makes calls to the cryptographic module is the single user of the cryptographic module, even when the application is serving multiple clients.

8. Cryptographic Key Management

8.1. Random Number Generation

The Module employs an ANSI X9.31 compliant random number generator and SP 800-90A compliant DRBG services for creation of cryptographic keys and CSPs. For more details on the RNG and DRBG, please refer to the Functional Design document.

The calling application is responsible for storage of generated keys returned by the Module. The seeds and entropy input are provided to the Module by the calling application. Module users (the calling application) shall use entropy sources that meet the security strength required for the random number generation mechanism: 128 bits for the RNG based on ANS X9.31, and as shown in Table 2, Table 3, and Table 4 in SP 800-90A for DRBG. The entropy is supplied by means of callback functions. Those functions must return an error if the minimum entropy strength cannot be met.

Caveat 7: The encryption strength of AES keys are modified by available entropy of seeds that are provided to the RNG and DRBG.

The module uses a FIPS-Approved DRBG or ANSI X9.31 RNG as an input to create the following keys/CSPs:

- AES key
- Triple-DES key
- RSA key pair
- DSA key pair
- EC Diffie-Hellman CSPs
- ECDSA key pair
- HMAC key

The module does not output intermediate values of keys/CSPs.

8.2. Key Entry and Output

The module does not support manual key entry or key output. Keys or other CSPs can only be exchanged between the module and the calling application using appropriate API calls.

8.3. Key Storage

Keys are not stored inside the crypto module. A pointer to plaintext key is passed through. Intermediate/temporary key storages are immediately zeroized.

8.4. Zeroization Procedure

Zeroization of sensitive data is performed automatically by API function calls for temporarily stored CSPs. All keys and CSPs are ephemeral and are destroyed when released by the appropriate API function calls.

In order to zeroize, keys and other CSPs appropriate zeroization APIs are called, which in turn calls another internal API function that overwrites the memory with an algorithm that depends on the pointer to the value, but not the value itself.

In regards to key generation, the external application must call the respective zeroization functions. Intermediate key storages are immediately assigned to zero. For more details on zeroization and related APIs, please refer to the Functional Design document.

8.5. Key Establishment

The module uses SP 800-56A based on the EC Diffie-Hellman key agreement and RSA for key transport/key wrapping. Please see caveat 3 and 4 for information regarding their security strength.

9. Electromagnetic Interference/Electromagnetic Compatibility (EMI/EMC)

Lab Name: PC Engineering Laboratory, Inc
FCC Registration: #90864

For information related to FCC ID of the devices, please refer to the Functional Design document.

10. Self Tests

The module performs an integrity test, as well as known answer tests upon initialization of the module and before the module becomes usable. If self tests fail, the module is in error state. In error state, no cryptographic operation is allowed, except the invocation of on demand self test.

Self test consists of the following tests:

10.1. Power-Up Tests

The module performs all power-up self tests during the loading phase of the module. Upon initialization and successful completion of power on self tests, the module then enters Non-FIPS mode and it is ready to be used. In order to switch to FIPS-Approved mode, the module performs the integrity test and all known answer tests (KATs) by invoking the API, FIPS_mode_set(FIPS_MODE_ON). Upon successful execution of KATs as listed in table 6, the module will be in FIPS-Approved mode. Please note that a DSA pair-wise consistency test is implemented instead of a KAT as a part of the power on self tests.

The module performs the integrity test, as well as all the KATs in both FIPS-Approved and Non-FIPS mode. The module enters error state if the conditional tests fail in either mode.

10.1.1. Cryptographic algorithm tests (Known Answer Tests)

Cryptographic algorithm test using a known answer will be conducted for all cryptographic functions (e.g., encryption, decryption, and random number generation) of each Approved cryptographic algorithm implemented by the module in FIPS-Approved mode.

Algorithm	**Test**
AES (separate encrypt and decrypt, ECB mode, 128 bit key)	KAT
AES CCM (separate encrypt and decrypt, 192 key length)	KAT
AES GCM (separate encrypt and decrypt, 256 key length)	KAT
AES CMAC (generate and verify CBC mode, 128, 192, 256 key lengths)	KAT
XTS-AES 128- or 256-bit key size to support XTS-AES-128 or XTS-AES-256 respectively	KAT
Triple-DES (separate encrypt and decrypt, ECB mode, 3 Key)	KAT
Triple-DES CMAC generate and verify, CBC mode, 3-Key	KAT
RSA (signature generation/verification using 2048-bit key, SHA-256)	KAT
DSA (signature generation/verification using 2048-bit key, SHA-384)	Pair-wise consistency test
ECC CDH shared secret calculation per section 5.7.1.2 of SP 800-56A, IG 9.6	KAT
ECDSA PCT (key generation, signature, and verification using	Pair-wise consistency test and KAT

Algorithm	Test
P-224, K-233 and SHA-512)	
ANSI X9.31 RNG (128-, 192-, 256-bit AES keys)	KAT
DRBG 800-90A: CTR_DRBG (AES, 256-bit with and without derivation function) HASH_DRBG (SHA-256) HMAC_DRBG (SHA-256)	KAT
HMAC-SHA-1	KAT
HMAC-SHA-224	KAT
HMAC-SHA-256	KAT
HMAC-SHA-384	KAT
HMAC-SHA-512	KAT
SHA-1	KAT
SHA-224	Tested as part of HMAC-SHA-224
SHA-256	Tested as part of HMAC-SHA-256
SHA-384	Tested as part of HMAC-SHA-384
SHA-512	Tested as part of HMAC-SHA-512

Table 7: Power-Up Tests

10.1.2. Integrity test

The module's integrity test is performed using HMAC-SHA-256.

- Build Time
  - HMAC-SHA-256 calculated on libfips_crypto.so (dynamic library) file
    - HMAC appended to libfips_crypto.so file
- Run Time
  - libfips_crypto.so is read as a file
  - When algorithm self tests are completed, integrity test routine is called
    - Perform HMAC-SHA-256 on the read libfips_crypto.so value in ram
    - Read stored HMAC located after libfips_crypto.so (last 32 bytes)
    - If calculated and stored values do not match, set error state, FIPS_R_FINGERPRINT_DOES_NOT_MATCH and the system property as "error_integrity"

10.2. Conditional Tests

Algorithm	Test
DSA	Key generation, Pair-wise consistency test
ECDSA	Key generation, Pair-wise consistency test
RSA	Key generation, Pair-wise consistency test
ANSI X9.31 RNG	Continuous test
SP 800-90A DRBG	Continuous test

Table 8: Conditional Tests

10.2.1. Pair-wise consistency test

A pair-wise consistency test must be conducted for every key generation.

The module implements RSA, DSA and ECDSA pair-wise consistency tests during key generation. If the test fails, it updates the FIPS status to error.

10.2.2. Continuous random number generator (CRNG) test

The continuous random number generator test implemented in the DRBG as well as in ANS X9.31 RNG and it is as follows: the CRNG test consists of a priming test which is implemented by using the very first RNG value to initialize the comparing value, discarding the RNG value and obtaining the next round of the RNG for output to the caller. The module performs the CRNG test every time the random number generation service is invoked. The very first random number is stored and it is compared against the second random number. If the two random numbers are the same, then the CRNG test fails and the module enters an error state. If the two random numbers are different, the CRNG test passes.

11. Design Assurance

11.1. Configuration Management

All source code is maintained in internal source code servers and the tools, Perforce and SVN, are used as code control. Perforce is used for commercial products and SVN is used for in-development projects. Release is based on the Change List number, which is auto-generated. Every check-in process creates a new change list number.

Versions of controlled items include information about each version. For documentation, revision history inside the document provides the current version of the document. Version control maintains the all the previous version and the version control system automatically numbers revisions.

For source code, unique information is associated with each version such that source code versions can be associated with binary versions of the final product.

11.2. Delivery and Operation

The crypto module is never released as source code. The module sources are stored and maintained at a secure development facility with controlled access.

The development team and the manufacturing factory share a secured internal server for exchanging binary software images. The factory is also a secure site with strict access control to the manufacturing facilities. The module binary is installed on the mobile devices (phone and tablets) using direct binary image installation at the factory. The mobile devices are then delivered to mobile service operators. Users cannot install or modify the module. The developer also has the capability to deliver software updates to service operators who in turn can update end-user phones and tablets using Over-The-Air (OTA) updates. Alternatively, the users may bring their mobile devices to service stations where authorized operators may use developer-supplied tools to install software updates on the phone. The developer vets all service providers and establishes secure communication with them for delivery of tools and software updates. If the binary is modified by unauthorized entity, the device has a feature to detect the change and thus not accept the binary modified by an unauthorized entity.

12. Mitigation of Other Attacks

No other attacks are mitigated.

13. Glossary and Abbreviations

AES	Advanced Encryption Specification
CAVP	Cryptographic Algorithm Validation Program
CBC	Cypher Block Chaining
CCM	Counter with Cipher Block Chaining-Message Authentication Code
CFB	Cypher Feedback
CC	Common Criteria
CMT	Cryptographic Module Testing
CMVP	Cryptographic Module Validation Program
CSP	Critical Security Parameter
CVT	Component Verification Testing
DES	Data Encryption Standard
DSA	Digital Signature Algorithm
EAL	Evaluation Assurance Level
FSM	Finite State Model
HMAC	Hash Message Authentication Code
KAT	Known Answer Test
MAC	Message Authentication Code
NIST	National Institute of Science and Technology
NVLAP	National Voluntary Laboratory Accreditation Program
OFB	Output Feedback
O/S	Operating System
OTA	Over-The-Air
RNG	Random Number Generator
RSA	Rivest, Shamir, Addleman
SDK	Software Development Kit
SHA	Secure Hash Algorithm
SHS	Secure Hash Standard
SLA	Service Level Agreement
SOF	Strength of Function
SVT	Scenario Verification Testing
TDES	Triple DES
UI	User Interface

14. References

[1] FIPS 140-2 Standard, http://csrc.nist.gov/groups/STM/cmvp/standards.html

[2] FIPS 140-2 Implementation Guidance, http://csrc.nist.gov/groups/STM/cmvp/standards.html

[3] FIPS 140-2 Derived Test Requirements, http://csrc.nist.gov/groups/STM/cmvp/standards.html

[4] FIPS 197 Advanced Encryption Standard, http://csrc.nist.gov/publications/PubsFIPS.html

[5] FIPS 180-4 Secure Hash Standard, http://csrc.nist.gov/publications/PubsFIPS.html

[6] FIPS 198-1 The Keyed-Hash Message Authentication Code (HMAC), http://csrc.nist.gov/publications/PubsFIPS.html

[7] FIPS 186-4 Digital Signature Standard (DSS), http://csrc.nist.gov/publications/PubsFIPS.html