

# **NetApp Storage Encryption (NSE) Running ONTAP 9.14.1 Security Target**

**Version 1.7**

**November 7, 2024**

**NetApp, Inc.**

3060 Olsen Drive

San Jose, CA 95128



---

## Contents

1	Security Target Introduction .....	1
1.1	Security Target, Target of Evaluation, and Common Criteria Identification .....	1
1.2	Conformance Claims.....	2
1.3	Conventions.....	4
1.3.1	Terminology .....	5
1.3.2	Acronyms.....	7
2	TOE Description .....	10
2.1	TOE Overview .....	10
2.2	TOE Description .....	10
2.3	TOE Architecture .....	11
2.3.1	Physical Boundaries.....	12
2.3.1.1	Hardware Requirements.....	12
2.3.2	Logical Boundaries .....	20
2.3.2.1	Cryptographic Support.....	21
2.3.2.2	Security Management.....	21
2.3.2.3	Protection of the TOE Security Functionality.....	21
2.4	Excluded Functionality .....	21
3	Documentation .....	22
4	Security Problem Definition.....	23
5	Security Objectives .....	24
5.1	Security Objectives for the Operational Environment .....	24
6	IT Security Requirements.....	25
6.1	Extended Requirements .....	25
6.2	TOE Security Functional Requirements.....	25
6.2.1	Cryptographic Support (FCS).....	27
6.2.1.1	FCS_AFA_EXT.1 Authorization Factor Acquisition .....	27
6.2.1.2	FCS_AFA_EXT.2 Timing of Authorization Factor Acquisition .....	27
6.2.1.3	FCS_CKM.1(b) Cryptographic Key Generation (Symmetric Keys).....	27
6.2.1.4	FCS_CKM.4(a) Cryptographic Key Destruction (Power Management) .....	27
6.2.1.5	FCS_CKM.4(d) Cryptographic Key Destruction (Software TOE, 3 <sup>rd</sup> Party Storage).....	27
6.2.1.6	FCS_CKM_EXT.4(a) Cryptographic Key and Key Material Destruction (Destruction Timing) .....	28
6.2.1.7	FCS_CKM_EXT.4(b) Cryptographic Key and Key Material Destruction (Power Management) .....	28
6.2.1.8	FCS_COP.1(a) Cryptographic Operation (Signature Verification) .....	28
6.2.1.9	FCS_COP.1(b) Cryptographic Operation (Hash Algorithm).....	28
6.2.1.10	FCS_COP.1(c) Cryptographic Operation (Keyed Hash Algorithm) .....	28
6.2.1.11	FCS_COP.1(d) Cryptographic Operation (Key Wrapping) .....	29
6.2.1.12	FCS_KDF_EXT.1 Cryptographic Key Derivation .....	29
6.2.1.13	FCS_KYC_EXT.1 Key Chaining (Initiator).....	29
6.2.1.14	FCS_PCC_EXT.1 Cryptographic Password Construct and Conditioning .....	29
6.2.1.15	FCS_RBG_EXT.1 Cryptographic Operation (Random Bit Generation) .....	29

6.2.1.16	FCS_SNI_EXT.1 Cryptographic Operation (Salt, Nonce, and Initialization Vector Generation) .....	30
6.2.1.17	FCS_VAL_EXT.1 Validation .....	30
6.2.2	Security Management (FMT) .....	31
6.2.2.1	FMT_MOF.1 Management of Functions Behavior.....	31
6.2.2.2	FMT_SMF.1 Specification of Management Functions .....	31
6.2.2.3	FMT_SMR.1 Security Roles .....	31
6.2.3	Protection of the TSF (FPT).....	31
6.2.3.1	FPT_KYP_EXT.1 Protection of Key and Key Material .....	31
6.2.3.2	FPT_PWR_EXT.1 Power Saving States .....	31
6.2.3.3	FPT_PWR_EXT.2 Timing of Power Saving States .....	31
6.2.3.4	FPT_TST_EXT.1 TSF Testing.....	32
6.2.3.5	FPT_TUD_EXT.1 Trusted Update.....	32
6.3	TOE Security Assurance Requirements .....	32
6.3.1	ADV_FSP.1 Basic functional specification .....	33
6.3.2	AGD_OPE.1 Operational user guidance .....	34
6.3.3	AGD_PRE.1 Preparative procedures .....	35
6.3.4	ALC_CMC.1 Labelling of the TOE.....	35
6.3.5	ALC_CMS.1 TOE CM coverage.....	35
6.3.6	ATE_IND.1 Independent testing - conformance .....	35
6.3.7	AVA_VAN.1 Vulnerability survey.....	36
7	TOE Summary Specification .....	36
7.1	Cryptographic Support .....	36
7.1.1	FCS_AFA_EXT.1: Authorization Factor Acquisition .....	38
7.1.2	FCS_AFA_EXT.2: Timing of Authorization Factor Acquisition .....	38
7.1.3	FCS_CKM.1(b): Cryptographic Key Generation (Symmetric Keys) .....	38
7.1.4	FCS_CKM.4(a): Cryptographic Key Destruction (Power Management) .....	39
7.1.5	FCS_CKM.4(d): Cryptographic Key Destruction (Software TOE, 3 <sup>rd</sup> Party Storage) .....	39
7.1.6	FCS_CKM_EXT.4(a) Cryptographic Key and Key Material Destruction (Destruction Timing)44	
7.1.7	FCS_CKM_EXT.4(b): Cryptographic Key and Key Material Destruction (Power Management) 44	
7.1.8	FCS_COP.1(a): Cryptographic Operation (Signature Verification) .....	44
7.1.9	FCS_COP.1(b): Cryptographic Operation (Hash Algorithm) .....	44
7.1.10	FCS_COP.1(c): Cryptographic Operation (Keyed Hash Algorithm).....	45
7.1.11	FCS_COP.1(d): Cryptographic Operation (Key Wrapping) .....	45
7.1.12	FCS_KDF_EXT.1: Cryptographic Key Derivation .....	45
7.1.13	FCS_KYC_EXT.1: Key Chaining (initiator).....	45
7.1.14	FCS_PCC_EXT.1: Cryptographic Password Construct and Conditioning .....	45
7.1.15	FCS_RBG_EXT.1 Cryptographic Operation (Random Bit Generation).....	45
7.1.16	FCS_SNI_EXT.1 Cryptographic Operation (Salt, Nonce, and Initialization Vector Generation) 46	
7.1.17	FCS_VAL_EXT.1 Validation .....	46
7.2	Security Management .....	47
7.2.1	FMT_MOF.1 Management of Functions Behavior .....	47
7.2.2	FMT_SMF.1: Specification of Management Functions .....	47
7.2.3	FMT_SMR.1: Security Roles .....	48

- 7.3 Protection of the TSF ..... 48
  - 7.3.1 FPT\_KYP\_EXT.1: Protection of Key and Key Material ..... 48
  - 7.3.2 FPT\_PWR\_EXT.1: Power Saving States / FPT\_PWR\_EXT.2: Timing of Power Saving States . 48
  - 7.3.3 FPT\_TST\_EXT.1: TSF Testing ..... 48
  - 7.3.4 FPT\_TUD\_EXT.1: Trusted Update..... 49
- 8 Protection Profile Claims ..... 50
- 9 Rationale ..... 51
  - 9.1 TOE Summary Specification Rationale ..... 51

---

## List of Tables

Table 1: NetApp Controllers Covered by the Evaluation .....	2
Table 2: NIAP TDs for [CPP_FDE_AA_V2.0E].....	3
Table 3: Terms and Definitions .....	5
Table 4: Acronyms.....	7
Table 5: Intel Microarchitecture/CPUs .....	12
Table 6: Non-TOE Hardware (Broadwell).....	13
Table 7: Non-TOE Hardware (Skylake) .....	15
Table 8: Non-TOE Hardware (Cascade Lake) .....	16
Table 9: Non-TOE Hardware (Ice Lake) .....	18
Table 10: Security Objectives for Operational Environment .....	24
Table 11: TOE Security Functional Components .....	26
Table 12: Assurance Components.....	32
Table 13: Third Party Components .....	33
Table 14: CryptoMod version 2.2 Algorithm Certificates .....	37
Table 15: NetApp Cryptographic Security Module (NCSM v3.0.8) Algorithm Certificates .....	37
Table 16: Critical Security Parameters .....	39
Table 17: HMAC Details .....	45
Table 18: Maximum Failed Attempts.....	47
Table 19: Security Functional Requirements .....	50
Table 20: Security Functions vs. Requirements Mapping.....	52

# 1 Security Target Introduction

This section identifies the Security Target (ST) and Target of Evaluation (TOE) identification, ST conventions, ST conformance claims, and the ST organization. The TOE is NetApp Storage Encryption (NSE) running ONTAP 9.14.1, an authorization acquisition product that obtains and manages authorization data used to access encrypted data stored on a full disk encryption product. The TOE provides the management and protection of keys that are used to protect the data encryption keys used by third-party self-encrypting drives (SEDs). The TOE supports FIPS validated third party SEDs that follow either the Trusted Computing Group's (TCG) Opal or Enterprise standards.

The ONTAP 9.14.1 component of the TOE is a proprietary operating system and data management software that is installed on the appliances listed below in Section 1.1 and that offers unified storage for applications that read and write data over block- or file- access protocols, in storage configurations that range from high-speed flash to lower- priced spinning media.

TOE implementations run on NetApp-engineered FAS (Fabric Attached Storage), AFF (All Flash FAS), and ASA (All SAN Array) appliances.

The Security Target (ST) includes the following additional sections:

- TOE Description (Section 2)
- Documentation (Section 3)
- Security Problem Definition(Section 4)
- Security Objectives (Section 5)
- IT Security Requirements (Section 6)
- TOE Summary Specification (Section 7)
- Protection Profile Claims (Section 8)
- Rationale (Section 9)

## 1.1 Security Target, Target of Evaluation, and Common Criteria Identification

**ST Title:** NetApp Storage Encryption (NSE) running ONTAP 9.14.1 Security Target

**ST Version:** Version 1.7

**ST Date:** November 7, 2024

**TOE Identification:** NetApp Storage Encryption (NSE) running ONTAP 9.14.1.

The non-TOE hardware required by and provisioned with the TOE is identified in **Table 1**:

Table 1: NetApp Controllers Covered by the Evaluation

NetApp Controllers	Disk Type	Controller Form Factor
AFF A150	SSD	2U/24 internal drives
AFF A220	SSD	2U/24 internal drives
AFF A250	NVMe/SSD	2U/24 internal drives
AFF A300	SSD	3U
AFF A320	NVMe	2U
AFF A400	NVMe/SSD	4U
AFF A800	NVMe/SSD	4U/48 internal drives
AFF A900	NVMe/SSD	8U
AFF C190	SSD	2U/24 internal drives
AFF C250	NVMe	2U/24 internal drives
AFF C400	NVMe	4U
AFF C800	NVMe	4U
ASA A150	SSD	2U/24 internal drives
ASA A250	NVMe	2U/24 internal drives
ASA A400	NVMe/SSD	4U
ASA A800	NVMe/SSD	4U/48 internal drives
ASA A900	NVMe/SSD	8U
ASA C250	NVMe	2U/24 internal drives
ASA C400	NVMe	4U
ASA C800	NVMe	4U
ASA AFF A220	SSD	2U/24 internal drives
FAS2720	HDD/SSD	2U/12 internal drives
FAS2750	HDD/SSD	2U/24 internal drives
FAS2820	HDD/SSD	2U/12 internal drives
FAS500f	NVMe	2U/24 internal drives
FAS8200	HDD/SSD	3U
FAS8300	HDD/SSD	4U
FAS8700	HDD/SSD	4U
FAS9500	HDD/SSD	8U

**TOE Developer:** NetApp, Inc.

**Evaluation Sponsor:** NetApp, Inc.

**CC Identification:** Common Criteria for Information Technology Security Evaluation, Version 3.1, Revision 5, April 2017

## 1.2 Conformance Claims

This ST and the TOE it describes are conformant to the following CC specifications:

- Common Criteria for Information Technology Security Evaluation Part 2: Security functional components, Version 3.1, Revision 5, April 2017.
  - Part 2 Extended
- Common Criteria for Information Technology Security Evaluation Part 3: Security assurance components, Version 3.1 Revision 5, April 2017.
  - Part 3 Conformant
- *collaborative Protection Profile for Full Drive Encryption – Authorization Acquisition, Version 2.0 + Errata, February 1, 2019 [CPP\_FDE\_AA\_V2.0E]* with the following optional and selection-based SFRs:
  - FCS\_CKM.1(b)
  - FCS\_COP.1(a)
  - FCS\_COP.1(b)
  - FCS\_COP.1(c)
  - FCS\_COP.1(d)
  - FCS\_KDF\_EXT.1
  - FCS\_PCC\_EXT.1
  - FCS\_RBG\_EXT.1
  - FCS\_VAL\_EXT.1
  - FPT\_TST\_EXT.1

**Error! Reference source not found.** contains the NIAP Technical Decisions (TDs) for the [CPP\_FDE\_AA\_V2.0E] protection profile at the time of evaluation.

*Table 2: NIAP TDs for [CPP\_FDE\_AA\_V2.0E]*

NIAP TD	TD Description	Applicable?	Applicability Rationale
<a href="#">TD0769</a>	FIT Technical Decision for FPT_KYP_EXT.1.1	Yes	Although the TOE does not make use of the modified selection, the SFR itself was modified by the TD.
<a href="#">TD0767</a>	FIT Technical Decision for FMT_SMF.1.1	Yes	Modifies an SFR and associated evaluation activity wording.
<a href="#">TD0766</a>	FIT Technical Decision for FCS_CKM.4(d) Test Notes	Yes	Modifies the evaluation activity associated with the SFR.
<a href="#">TD0765</a>	FIT Technical Decision for FMT_MOF.1	Yes	Modifies the evaluation activity associated with the SFR.



NIAP TD	TD Description	Applicable?	Applicability Rationale
<a href="#">TD0764</a>	FIT Technical Decision for FCS_PCC_EXT.1	Yes	Although the TOE does not make use of the modified selection, the SFR itself was modified by the TD.
<a href="#">TD0760</a>	FIT Technical Decision for FCS_SNI_EXT.1.3, FCS_COP.1(f)	Yes	Modifies an SFR and associated evaluation activity wording.
<a href="#">TD0759</a>	FIT Technical Decision for FCS_AFA_EXT.1.1	Yes	Although the TOE does not make use of the modified selection, the SFR itself was modified by the TD.
<a href="#">TD0606</a>	FIT Technical Recommendation for Evaluating a NAS against the FDE AA and FDE EE	Yes	The TOE can be utilized as a NAS device.
<a href="#">TD0458</a>	FIT Technical Decision for FPT_KYP_EXT.1 evaluation activities	Yes	Modifies the evaluation activity associated with the SFR.

### 1.3 Conventions

The following conventions have been applied in this document:

- Security Functional Requirements – Part 2 of the CC defines the approved set of operations that may be applied to functional requirements: iteration, assignment, selection, and refinement.
  - Iteration: allows a component to be used more than once with varying operations. In the ST, all iterations (which are taken from [CPP\_FDE\_AA\_V2.0E] are identified by a single lower alphabetic character in parentheses (e.g., FCS\_COP.1(a)).
  - Assignment: allows the specification of an identified parameter. Assignments are indicated using bold and are surrounded by brackets (e.g., **[assignment]**). Note that an assignment within a selection would be identified in italics and with embedded bold brackets (e.g., **[*selected-assignment*]**).
  - Selection: allows the specification of one or more elements from a list. Selections are indicated using bold italics and are surrounded by brackets (e.g., **[*selection*]**).
  - Refinement: allows the addition of details. Refinements are indicated using bold, for additions, and strike-through, for deletions (e.g., “... **all** objects ...” or “... ~~some~~ **big** things ...”).
- Other sections of the ST – Other sections of the ST use bolding to highlight text of special interest, such as captions.

## 1.3.1 Terminology

Table 3: Terms and Definitions

Term	Definition
<b>Authentication Key (AK)</b>	Authentication key used to authenticate ONTAP to a SED. The 32-byte DRBG generated AK is used by the drive to protect the drive's DEK. For FDE-AA with NSE, the AK is the BEV. Note: AK is used generically in this document.
<b>AK1</b>	One of two authentication keys (AKs). AK1 (or AK2) can be used as the AK for the SED's data port, the SED's FIPS port, or both.
<b>AK2</b>	One of two authentication keys (AKs). AK2 (or AK1) can be used as the AK for the SED's data port, the SED's FIPS port, or both.
<b>ALL-Flash FAS (AFF)</b>	NetApp proprietary custom-build hardware appliances with only SSD drives and optimized ONTAP for low latency.
<b>All-SAN Array (ASA)</b>	NetApp proprietary custom-build hardware appliances with All SAN Array build on top of AFF platform, and provides only SAN-based data protocol connectivity.
<b>Authorization Factor</b>	A value that a user knows, has, or is (e.g. password, token, etc.) submitted to the TOE to establish that the user is in the community authorized to use the hard disk. This value is used in the derivation or decryption of the Border Encryption Value (BEV) and eventual decryption of the DEK. Note that these values may or may not be used to establish the particular identity of the user.
<b>Border Encryption Value (BEV)</b>	Value passed by the FDE Authorization Acquisition (FDE-AA) component to the FDE Encryption Engine (FDE-EE) component. For FDE-AA with NSE, the Authentication Key (AK) is the BEV.
<b>Cluster Key Encryption Key (CKEK)</b>	A 32-byte KEK that is generated via a DRBG. The CKEK is common to all nodes in the ONTAP cluster.
<b>Cluster Passphrase</b>	Cluster Passphrase is a 64 to 256-byte ASCII passphrase that is used as an authorization factor.
<b>Cluster Passphrase Key Encryption Key (CP-KEK)</b>	A 32-byte KEK that is derived from the cluster passphrase whose unwrapped value is derived using a passphrase based key derivation function. The CP-KEK is used to protect the CKEK.
<b>Data Encryption Key (DEK)</b>	A key used to encrypt data-at-rest.
<b>Fabric-Attached Storage (FAS)</b>	NetApp proprietary custom-build hardware appliances with HDD or SSD drives.
<b>FC</b>	Fibre Channel is the original networked block protocol. Instead of files, a block protocol presents an entire virtual disk to a client. The traditional FC protocol uses a dedicated FC network with specialized FC switches and requires a client computer to also have FC network interfaces. The virtual disk is represented by a LUN, with one or more LUNs being stored in an ONTAP volume.

Term	Definition
<b>FCoE</b>	FCoE is basically the same protocol as FC but uses datacenter-grade Ethernet network in place of the traditional FC transport. As with FC, the client requires an FCoE-specific network interface.
<b>Intermediate Key</b>	A key used in a point between the initial user authorization and the DEK.
<b>iSCSI</b>	iSCSI is a block protocol that can run on standard Ethernet networks. Most client operating systems offer a software initiator that runs over a standard Ethernet port.
<b>Key Chaining</b>	The method of using multiple layers of encryption keys to protect data. A top layer key encrypts a lower layer key, which encrypts the data; this method can have any number of layers.
<b>Key Encryption Key (KEK)</b>	A key used to encrypt other keys, such as DEKs.
<b>Key Material</b>	Key material is commonly known as critical security parameter (CSP) data, and includes authorization data, nonces, and metadata.
<b>Key Sanitization</b>	A method of sanitizing encrypted data by securely overwriting or destroying the key that was encrypting the data.
<b>Logical Interface</b>	A LIF (logical interface) is an IP address or WWPN with associated characteristics, such as a role, a home port, a home node, a list of ports to fail over to, and a firewall policy. You can configure LIFs on ports over which the cluster sends and receives communications over the network.
<b>Logical Unit Number</b>	A LUN (logical unit number) is an identifier for a device called a logical unit addressed by a SAN protocol.
<b>Network Attached Storage (NAS)</b>	A NAS is a single storage device that operates on data files. A NAS unit includes a dedicated hardware device that connects to a local area network, usually through an Ethernet connection. This NAS server authenticates clients and manages file operations in much the same manner as ordinary file servers, through well-established network protocols.
<b>NetApp CryptoMod</b>	This module provides NIST CAVP validated cryptographic operations for NSE and the onboard key manager (OKM).
<b>NFS</b>	NFS is the traditional file access protocol for UNIX and Linux systems. Clients can access files in ONTAP volumes using the NFSv3, NFSv4, NFSv4.1, and pNFS protocols. File access is controlled using UNIX-style permissions, NTFS-style permissions, or a combination of both.
<b>Non-Volatile Memory</b>	A type of computer memory that will retain information without power.
<b>NVMe/FC</b>	NVMe/FC, designed to work with flashed-based storage, offers scalable sessions, significantly reduced data latency, and higher throughput. NVMe/FC uses namespaces instead of LUNs. The NVMe namespaces, which are stored in an ONTAP volume, may only be accessed via the NVMe protocol.
<b>OKM</b>	Onboard Key Manager – local key-management without requiring an external key management server.
<b>Protected Data</b>	This refers to all data on the storage device with the exception of a small portion required for the TOE to function correctly. It is the space on the disk a user could write data to and includes the operating system, applications, and user data. Protected data does not include the Master Boot Record or Pre-authentication area of the drive – areas of the drive that are necessarily unencrypted.

Term	Definition
<b>RDB</b>	ONTAP's replicated database. RDB is a quorum-based synchronous transactional data replication service used by ONTAP components to persist configuration data. The RDB is available to a node only after the node joins the cluster.
<b>Single-node cluster</b>	A single-node cluster is a special implementation of a cluster running on a standalone node. The TOE can deploy a single-node cluster in a branch office, for example, assuming the workloads are small enough, and that storage availability is not a critical concern.
<b>SMB/CIFS</b>	SMB/CIFS is the traditional file access protocol for Windows systems. Clients can access files in ONTAP volumes using the SMB 2.0, SMB 2.1, SMB 3.0, and SMB 3.1.1 protocols. SMB/CIFS, like NFS, supports a mix of access permission styles.
<b>Storage Array Networks (SAN)</b>	A SAN is a local network of several devices. A SAN commonly uses Fibre Channel interconnects and connects a set of storage devices that share data with one another.
<b>Submask</b>	A submask is a bit string that can be generated and stored in a number of ways.
<b>Storage Virtual Machine</b>	Storage virtual machines (SVMs) serve data to clients and hosts. Like a virtual machine running on a hypervisor, an SVM is a logical entity that abstracts physical resources. Data accessed through the SVM is not bound to a location in storage. Network access to the SVM is not bound to a physical port.
<b>Target of Evaluation</b>	A set of software, firmware, or hardware possibly accompanied by guidance. [CC1]
<b>Volume Data Encryption Key (VDEK)</b>	A symmetric key used to encrypt/decrypt the volume data. This may also be referred to as a Volume Encryption Key (VEK). The terms are used interchangeably in this document.
<b>wAK</b>	Wrapped (i.e. encrypted) version of AK. The wAK is formed by encrypting an AK with the CKEK. The term wAK is used as a generic reference to either wAK1 or wAK2 in this document.
<b>wAK1</b>	Wrapped version of AK1. The wAK1 is formed by encrypting AK1 with the CKEK.
<b>wAK2</b>	Wrapped version of AK2. The wAK2 is formed by encrypting AK2 with the CKEK.
<b>wCKEK</b>	The wrapped, or encrypted, version of the CKEK.
<b>World Wide Port Name</b>	A World Wide Port Name, (WWPN) is a World Wide Name assigned to a port in a Fibre Channel fabric. Used on storage area networks, it performs a function equivalent to the MAC address in Ethernet protocol, as it is supposed to be a unique identifier in the network.

### 1.3.2 Acronyms

Table 4: Acronyms

Acronym	Definition
<b>AA</b>	Authorization Acquisition
<b>AK</b>	Authentication Key
<b>AES</b>	Advanced Encryption Standard
<b>AFF</b>	All Flash FAS
<b>ASA</b>	All SAN Array

Acronym	Definition
BEV	Border Encryption Value
CBC	Cipher Block Chaining
CC	Common Criteria
CEM	Common Evaluation Methodology
CPP	Collaborative Protection Profile
DEK	Data Encryption Key
DRBG	Deterministic Random Bit Generator
DSS	Digital Signature Standard
ECC	Elliptic Curve Cryptography
ECDSA	Elliptic Curve Digital Signature Algorithm
EE	Encryption Engine
FAS	Fabric Attached Storage
FC	Fibre Channel
FCoE	Fibre Channel Over Ethernet
FDE	Full Drive Encryption
FIPS	Federal Information Processing Standards
HDD	Hard Disk Drive
HMAC	Keyed-Hash Message Authentication Code
IEEE	Institute of Electrical and Electronics Engineers
iSCSI	Internet Small Computer Interface
ISO/IEC	International Organization for Standardization / International Electrotechnical Commission
IT	Information Technology
IV	Initialization Vector
KEK	Key Encryption Key
LIF	Logical Interface
LUN	Logical Unit Number
MBR	Master Boot Record
NAS	Network Attached Storage
NFS	Network File System
NIST	National Institute of Standards and Technology
NSE	NetApp Storage Encryption
NVMe	Non-Volatile Memory Express
OS	Operating System
PRF	Pseudo Random Function

Acronym	Definition
<b>RBG</b>	Random Bit Generator
<b>RDB</b>	ONTAP's replicated database
<b>RNG</b>	Random Number Generator
<b>RSA</b>	Rivest Shamir Adleman Algorithm
<b>SAN</b>	Storage Array Networks
<b>SAR</b>	Security Assurance Requirements
<b>SED</b>	Self-Encrypting Drive
<b>SFR</b>	Security Functional Requirements
<b>SHA</b>	Secure Hash Algorithm
<b>SMB/CIFS</b>	Server Message Block
<b>SPD</b>	Security Problem Definition
<b>SPI</b>	Serial Peripheral Interface
<b>SSD</b>	Solid State Drive
<b>ST</b>	Security Target
<b>SVM</b>	Storage Virtual Machine
<b>SVM-KEK</b>	Storage Virtual Machine Key Encryption Key
<b>TOE</b>	Target of Evaluation
<b>TPM</b>	Trusted Platform Module
<b>TSF</b>	TOE Security Functionality
<b>TSS</b>	TOE Summary Specification
<b>USB</b>	Universal Serial Bus
<b>VDEK</b>	Volume Data Encryption Key
<b>wCKEK</b>	Wrapped Cluster Key Encryption Key
<b>wVDEK</b>	Wrapped Volume Data Encryption Key
<b>wSVM-KEK</b>	Wrapped Storage Virtual Machine Key Encryption Key
<b>WWPN</b>	World Wide Port Name
<b>XOR</b>	Exclusive or
<b>XTS</b>	XEX (XOR Encrypt XOR) Tweakable Block Cipher with Ciphertext Stealing

## 2 TOE Description

### 2.1 TOE Overview

The TOE is NetApp Storage Encryption (NSE) running ONTAP 9.14.1, an authorization acquisition product that obtains and manages authorization data use to access encrypted data stored on a FIPS validated full disk encryption product. The TOE provides authorization data to FIPS validated third party self-encrypting drives (SEDs).

### 2.2 TOE Description

The TOE is provided pre-installed on NetApp disk storage appliances consisting of storage controllers and one or more enclosures of FIPS validated SEDs. The NetApp appliances included in the evaluated configuration are listed in Table 1.

ONTAP 9.14.1 is a proprietary operating system and data management software that provides storage for applications that read and write data over block- or file-access protocols, in storage configurations that range from high-speed flash to lower- priced spinning media. All drives used by the TOE are FIPS validated third party SED devices.

NetApp Storage Encryption supports FIPS validated third party SEDs that follow either the Trusted Computing Group's (TCG) Opal or Enterprise standards. Both standards support the use of an authentication key (AK) and one or more data encryption keys (DEK) per drive. The AK is used by a client (client in this case indicating ONTAP, the NetApp operating system) to unlock a drive. Once the drive verifies that the AK is correct, it uses the AK to decrypt the drive's DEK(s).

NetApp Storage Encryption supports use of an external key management (KMIP) server or Onboard Key Manager (OKM) to manage the authentication key (AK) used by the array's SEDs. However, in the evaluated configuration, only OKM is supported, and OKM must be configured with CC mode enabled. When CC mode is enabled, OKM requires entry of the Cluster Passphrase every time the storage array is booted.

NetApp Storage Encryption must be configured via the appliance's RS-232 console port in the evaluated configuration. NetApp Storage Encryption also supports various networking protocols including SSH, CIFS, NFS, HTTP, HTTPs, DHCP, SNMP, Fibre Channel, and iSCSI, among others. The Protection Profile ([CPP\_FDE\_AA\_V2.0E]) associated with this product did not consider, nor did it include networking protocols as part of the security functional requirements and, as a result, did not include any requirements for addressing those protocols. Consequently, the protocols have not been examined as part of the required assurance activities and, therefore, no claims are made about the TOE's networking protocols.

It is suggested that a customer using the product consider the impact of using the product's SSH or HTTPs interfaces to administer the product as opposed to the product's RS-232 console interface. The customer should base their decision on the environment in which the TOE operates and the value of the data that needs to be protected.

## 2.3 TOE Architecture

Depending on the NetApp hardware controller model, node storage consists of FIPS validated flash disks, capacity HDD drives, or both. Network ports on the controller provide access to data. Physical storage and network connectivity resources are virtualized, visible to cluster administrators only, not to NAS clients or SAN hosts.

NetApp appliances typically are configured in cluster nodes in high-availability (HA) pairs for fault tolerance and non-disruptive operations. If a node fails or if a node needs to be brought down for routine maintenance, its partner can take over its storage and continue to serve data from it. The partner gives back storage when the node is brought back on-line. HA pairs always consist of like controller models. The controllers typically reside in the same chassis with redundant power supplies. The HA functionality was not covered in the scope of the evaluation or testing.

Figure 1 TOE High-Availability Pair Configuration



ONTAP 9.14.1 supports all the major industry standard NAS and SAN based client protocols: NFS, SMB/CIFS, FC, FCoE, iSCSI, and NVMe/FC.

Customers use storage virtual machines (SVMs) to serve data to clients and hosts. An SVM is a logical entity that abstracts physical resources. Data accessed through the SVM is not bound to a location in storage. Network access to the SVM is not bound to a physical port.

In addition to data volumes, ONTAP also uses the following special volumes (note: these volumes, like all volumes on an NSE system, are hosted on third party self-encrypting drives):

- A node root volume (typically “vol0”) contains node configuration information and logs.
- An SVM root volume serves as the entry point to the namespace provided by the SVM and contains namespace directory information.
- System volumes contain special metadata such as service audit logs.

ONTAP prevents customers from storing user data on these special volumes.

In addition to data SVMs, ONTAP deploys special SVMs for administration:

- An admin SVM is created when the cluster is set up.
- A node SVM is created when a node joins a new or existing cluster.
- A system SVM is automatically created for cluster-level communications in an IP space.

The administrative SVMs listed above cannot be used to serve data. All administration is performed via the CLI accessed using a console directly connected to the appliance’s RS-232 port.



NetApp Storage Encryption (NSE) provides the authentication key to FIPS validated “self-encrypting” drives (SEDs), also known as Full Disk Encryption, or FDE, drives, that encrypt data as it is written. The data cannot be read without an encryption key stored on the disk. The encryption key, in turn, is accessible only to an authenticated node.

On an I/O request, a node authenticates itself to an SED using one or more authentication keys (AKs) retrieved from the Onboard Key Manager. The authentication key used to authenticate ONTAP to a SED is used by the drive to protect the drive’s DEK. The authentication key, or AK, is a 32-byte value that is generated by the TOE’s DRBG. For the [CPP\_FDE\_AA\_V2.0E] with NSE, the AK is the BEV. Note: AK is used generically in this document.

### 2.3.1 Physical Boundaries

The physical boundary of the TOE encompasses the NetApp ONTAP 9.14.1 software.

#### 2.3.1.1 Hardware Requirements

The non-TOE hardware required by and provisioned with the TOE is equipped with Intel processors described in the table that follows.

*Table 5: Intel Microarchitecture/CPU*s

Intel Microarchitecture Family	Intel CPUs
<b>Broadwell Xeon D Processor</b>	Intel Xeon D-1557
	Intel Xeon D-1587
<b>Skylake Xeon Scalable Processors</b>	Intel Xeon Silver 4114
	Intel Xeon Platinum 8160
<b>Skylake Xeon D Processor</b>	Intel Xeon D-2164-IT
<b>Cascade Lake 2<sup>nd</sup> Gen Scalable Processors</b>	Intel Xeon Silver 4210
	Intel Xeon Gold 5218
	Intel Xeon Gold 5220R
<b>Ice Lake Xeon D Processor</b>	D-1735TR
<b>Ice Lake 3<sup>rd</sup> Gen Scalable Processors</b>	Platinum 8352Y

The non-TOE hardware required by and provisioned with the TOE are identified in the tables below. In each of the tables, storage protocols are identified as NAS, SAN, or both (NAS/SAN). A storage controller listed as supporting NAS will have support for one or more of the following protocols:

- CIFS;
- NFS v3, NFS v4.0, NFS v4.1, NFS v4.2;

- NVMe/TCP;
- S3;
- SMB 2.0, SMB 2.1, SMB 2.x, SMB 3.0, SMB 3.1, SMB 3.1.1.

A storage controller listed as supporting SAN will have support for one or more of the following protocols:

- FC, FCoE;
- iSCSI;
- NVMe/FC

NetApp storage controllers using an Intel Broadwell processor are identified in Table 6.

*Table 6: Non-TOE Hardware (Broadwell)*

Values identified, including the CPU count, are for a single HA pair specification.					
All disk drives are third party devices.					
Storage Controller	Disk Type	Storage Protocols	Max Drives per HA Pair (HDD/SSD)	Controller Form Factor	Storage Controller Processor
Intel Broadwell Xeon D Processor					
FAS2720	HDD/SSD	NAS/SAN	144/136 12 internal drives	2U	Intel Xeon D-1557 (Broadwell) 2 x 64-bit 12-core 1.50 GHz
FAS2750	HDD/SSD	NAS/SAN	144/144 24 internal drives	2U	Intel Xeon D-1557 (Broadwell) 2 x 64-bit 12-core 1.50 GHz)
FAS8200	HDD/SSD	NAS/SAN	480/480	3U	Intel Xeon D-1587 (Broadwell) 2 x 64-bit 16-core 1.70 GHz

Values identified, including the CPU count, are for a single HA pair specification.					
All disk drives are third party devices.					
Storage Controller	Disk Type	Storage Protocols	Max Drives per HA Pair (HDD/SSD)	Controller Form Factor	Storage Controller Processor
AFF A150	SSD	NAS/SAN	72	2U	Intel Xeon D-1557 (Broadwell) 2 x 64-bit 12-core 1.50 GHz
ASA A150	SSD	SAN	72	2U	Intel Xeon D-1557 (Broadwell) 2 x 64-bit 12-core 1.50 GHz
AFF A220	SSD	NAS/SAN	144 24 internal drives	2U	Intel Xeon D-1557 (Broadwell) 2 x 64-bit 12-core 1.50 GHz
ASA AFF A220	SSD	SAN	144 24 internal drives	2U	Intel Xeon D-1557 (Broadwell) 2 x 64-bit 12-core 1.50 GHz
AFF A300	SSD	NAS/SAN	384	3U	Intel Xeon D-1587 (Broadwell) 2 x 64-bit 16-core 1.70 GHz
AFF C190	SSD	NAS/SAN	24 24 internal drives	2U	Intel Xeon D-1557 (Broadwell) 2 x 64-bit 8-core 1.50 GHz

NetApp storage controllers with Intel Skylake processors are listed in Table 7.

*Table 7: Non-TOE Hardware (Skylake)*

Values identified, including the CPU count, are for a single HA pair specification. All disk drives are third party devices.					
Storage Controller	Disk Type	Storage Protocols	Max Drives per HA Pair (HDD/SSD)	Controller Form Factor	Storage Controller Processor
Intel Skylake Xeon Scalable Processors					
AFF A800 <sup>1</sup>	NVMe/SSD	NAS/SAN	240/240 48 internal drives	4U	Intel Xeon Platinum 8160 (Skylake-SP) 4 x 64-bit 24-core 2.10 GHz
ASA A800 <sup>2</sup>	NVMe/SSD	SAN	240/240 48 internal drives	4U	Intel Xeon Platinum 8160 (Skylake-SP) 4 x 64-bit 24-core 2.10 GHz
AFF A320	NVMe	NAS/SAN	96	2U	Intel Xeon Silver 4114 (Skylake-SP) 4 x 64-bit 10-core 2.20 GHz
Intel Skylake Xeon D Processor					
AFF A250	NVMe/SSD	NAS/SAN	48/24 24 internal drives	2U	Intel Xeon D-2164-IT (Skylake-D) 2 x 64-bit 12 core, 2.10 GHz

<sup>1</sup> AFF A800 models first shipped with the Intel Xeon Platinum 8160, later models shipped with the Intel Xeon Gold 5220R after the Platinum 8160 was discontinued.

<sup>2</sup> ASA A800 models first shipped with the Intel Xeon Platinum 8160, later models shipped with the Intel Xeon Gold 5220R after the Platinum 8160 was discontinued.

Values identified, including the CPU count, are for a single HA pair specification.					
All disk drives are third party devices.					
Storage Controller	Disk Type	Storage Protocols	Max Drives per HA Pair (HDD/SSD)	Controller Form Factor	Storage Controller Processor
ASA A250	NVMe	SAN	48 24 internal drives	2U	Intel Xeon D-2164-IT (Skylake-D) 2 x 64-bit 12 core, 2.10 GHz
AFF C250	NVMe	NAS/SAN	48 24 internal drives	2U	Intel Xeon D-2164-IT (Skylake-D) 2 x 64-bit 12 core, 2.10 GHz
ASA C250	NVMe	SAN	48 24 internal drives	2U	Intel Xeon D-2164-IT (Skylake-D) 2 x 64-bit 12 core, 2.10 GHz
FAS500f	NVMe	NAS/SAN	48 24 internal drives	2U	Intel Xeon D-2164-IT (Skylake-D) 2 x 64-bit 12 core, 2.10 GHz

Table 8 lists NetApp storage controllers with Intel Cascade Lake CPUs.

*Table 8: Non-TOE Hardware (Cascade Lake)*

Values identified, including the CPU count, are for a single HA pair specification.					
All disk drives are third party devices.					
Storage Controller	Disk Type	Storage Protocols	Max Drives	Controller Form Factor	Storage Controller Processor
Intel Cascade Lake 2 <sup>nd</sup> Gen Xeon Scalable Processors					

Values identified, including the CPU count, are for a single HA pair specification. All disk drives are third party devices.					
Storage Controller	Disk Type	Storage Protocols	Max Drives	Controller Form Factor	Storage Controller Processor
FAS8300	HDD/SSD	NAS/SAN	720/480	4U	Intel Xeon Silver 4210 (Cascade Lake)  4 x 64-bit 10-core, 2.20 GHz
FAS8700	HDD/SSD	NAS/SAN	1440/480	4U	Intel Xeon Gold 5218 (Cascade Lake)  4 x 64-bit 16-core, 2.30 GHz
AFF A400	NVMe/SSD	NAS/SAN	96/480	4U	Intel Xeon Silver 4210 (Cascade Lake)  4 x 64-bit 10-core, 2.20 GHz
ASA A400	NVMe/SSD	SAN	96	4U	Intel Xeon Silver 4210 (Cascade Lake)  4 x 64-bit 10-core, 2.20 GHz
ASA C400	NVMe	SAN	96	4U	Intel Xeon Silver 4210 (Cascade Lake)  4 x 64-bit 10-core, 2.20 GHz
AFF A800 <sup>3</sup>	NVMe/SSD	NAS/SAN	240/240	4U	Intel Xeon Gold 5220R (Cascade Lake)

<sup>3</sup> AFF A800 models first shipped with the Intel Xeon Platinum 8160, later models shipped with the Intel Xeon Gold 5220R after the Platinum 8160 was discontinued.

Values identified, including the CPU count, are for a single HA pair specification. All disk drives are third party devices.					
Storage Controller	Disk Type	Storage Protocols	Max Drives	Controller Form Factor	Storage Controller Processor
			48 internal drives		4 x 64-bit 24-core, 2.10 GHz
ASA A800 <sup>4</sup>	NVMe/SSD	SAN	240/240  48 internal drives	4U	Intel Xeon Gold 5220R (Cascade Lake)  4 x 64-bit 24-core, 2.10 GHz
ASA C800	NVMe	SAN	240  48 internal drives	4U	Intel Xeon Gold 5220R (Cascade Lake)  4 x 64-bit 24-core, 2.10 GHz
AFF C400	NVMe	NAS/SAN	96	4U	Intel Xeon Silver 4210 (Cascade Lake)  4 x 64-bit 10-core, 2.20 GHz
AFF C800	NVMe	NAS/SAN	240  48 internal drives	4U	Intel Xeon Gold 5220R (Cascade Lake)  4 x 64-bit 24-core, 2.10 GHz

NetApp storage controllers with Intel Ice Lake CPUs are listed in Table 9:

*Table 9: Non-TOE Hardware (Ice Lake)*

<sup>4</sup> ASA A800 models first shipped with the Intel Xeon Platinum 8160, later models shipped with the Intel Xeon Gold 5220R after the Platinum 8160 was discontinued.

Values identified, including the CPU count, are for a single HA pair specification.					
All disk drives are third party devices.					
Storage Controller	Disk Type	Storage Protocols	Max Drives	Controller Form Factor	Storage Controller Processor
Intel Ice Lake 3 <sup>rd</sup> Gen Xeon Scalable Processors					
FAS9500	HHD/SSD	NAS/SAN	1440/480	8U	Intel Xeon Platinum 8352Y (Ice Lake) 4 x 64-bit 32-core, 2.20 GHz
AFF A900	NVMe/SSD	NAS/SAN	240/480	8U	Intel Xeon Platinum 8352Y (Ice Lake) 4 x 64-bit 32-core 2.20 GHz
ASA A900	NVMe/SSD	SAN	240/480	8U	Intel Xeon Platinum 8352Y (Ice Lake) 4 x 64-bit 32-core 2.20 GHz
Intel Ice Lake Xeon D Processor					
FAS2820	HDD/SSD	NAS/SAN	144/138 12 internal drives	2U	Intel Xeon D-1735TR (Ice Lake-D) 2 x 64-bit 8-core 2.20 GHz

The cryptographic modules included in the TOE have NIST Cryptographic Algorithm Validation Program (CAVP) certificates A4794, A4795, and A4858.

The Operating Environments for the A4794, A4795, and A4858 certificates include the following:

- Intel Xeon D-1557 (Broadwell Xeon D Processor microarchitecture), which covers the following NetApp models:
  - AFF A150, AFF A220, AFF C190, ASA A150, ASA AFF A220, FAS2720, FAS2750: all include the Intel Xeon D-1557
  - AFF A300 and FAS8200: includes the Intel Xeon D-1587, which is equivalent at the microarchitecture level to the Intel Xeon D-1557



- Intel D-2164IT (Skylake Xeon D Processor microarchitecture), which covers the following NetApp models:
  - AFF A250, ASA A250, AFF C250, ASA C250, FAS500f: includes the Intel D-2164IT
- Intel Xeon Silver 4114 (Skylake Xeon Scalable Processors microarchitecture), which covers the following NetApp models:
  - AFF A320: includes the Intel Xeon Silver 4114
  - AFF A800<sup>5</sup>, ASA A800<sup>6</sup>: includes the Intel Xeon Platinum 8160, which is equivalent at the microarchitecture level to the Intel Xeon Silver 4114
- Intel Xeon Silver 4210 (Cascade Lake 2<sup>nd</sup> Gen Xeon Scalable Processors microarchitecture), which covers the following NetApp models:
  - AFF A400, ASA A400, AFF C400, ASA C400, FAS8300: includes the Intel Xeon Silver 4210
  - FAS8700: includes the Intel Xeon Gold 5218, which is equivalent at the microarchitecture level to the Intel Xeon Silver 4210.
  - AFF A800<sup>5</sup>, ASA A800<sup>6</sup>, AFF C800, ASA C800: includes the Intel Xeon Gold 5220R, which is equivalent at the microarchitecture level to the Intel Xeon Silver 4210
- Intel Xeon D-1735TR (Ice Lake Xeon D Processor microarchitecture), which covers the following NetApp models:
  - FAS2820: includes the Intel Xeon D-1735TR
- Intel Xeon Platinum 8352Y (Ice Lake 3<sup>rd</sup> Gen Xeon Scalable Processors microarchitecture), which covers the following NetApp models:
  - AFF A900, ASA A900, FAS9500: includes the Intel Xeon Platinum 8352Y

Therefore, all NetApp models included in the evaluated configuration are covered by the A4794, A4795, and A4858 CAVP certificates, because they either include the same processor on which the certified algorithm testing was performed, or they include a processor that is equivalent at the microarchitecture level to a processor on which algorithm testing was performed.

### 2.3.2 Logical Boundaries

This section identifies the TOE's logical boundaries:

---

<sup>5</sup> AFF A800 models first shipped with the Intel Xeon Platinum 8160, later models shipped with the Intel Xeon Gold 5220R after the Platinum 8160 was discontinued.

<sup>6</sup> ASA A800 models first shipped with the Intel Xeon Platinum 8160, later models shipped with the Intel Xeon Gold 5220R after the Platinum 8160 was discontinued.

- Cryptographic Support
- Security Management
- Protection of the TSF

### 2.3.2.1 Cryptographic Support

The TOE includes NIST CAVP-validated cryptographic algorithms supporting cryptographic functions. The TOE provides Key Wrapping, Key Derivation, and BEV Validation.

### 2.3.2.2 Security Management

The TOE supports management functions for forwarding requests to change the DEK to the EE, forwarding requests to cryptographically erase the DEK to the EE, allowing authorized users to change authorization factors or set of authorization factors used, and initiate TOE software updates using a command line interface.

### 2.3.2.3 Protection of the TOE Security Functionality

The TOE provides trusted firmware updates, protects Key and Key Material; and supports power saving states. The TOE runs a suite of self-tests during initial start-up (on power on).

## 2.4 Excluded Functionality

The list below identifies features or protocols that are not evaluated or must be disabled, and the rationale why. Note that this does not mean the features cannot be used in the evaluated configuration (unless explicitly stated so). It means that the features were not evaluated and/or validated by an independent third party and the functional correctness of the implementation is vendor assertion. Evaluated functionality is scoped exclusively to the security functional requirements specified in this Security Target. The features below are out of scope.

Feature	Description
SnapLock	NetApp SnapLock is the WORM (write once, read many) compliance replication solution from NetApp. It provides integrated data protection for workloads that need to adhere to regulatory guidelines such as HIPAA, SEC 17a-4(f) rule, FINRA, and CFTC as well as national requirements for German-speaking countries (DACH).  SnapLock was not included in the evaluation and was not tested in the evaluated configuration.
Trusted Platform Module (TPM)	The encryption keys for the onboard key manager (OKM) are not sealed by a physical TPM when running in Common Criteria mode.

Feature	Description
MetroCluster	<p>NetApp MetroCluster (MC) software is a solution that combines array-based clustering with synchronous replication to deliver continuous availability and zero data loss at the lowest cost.</p> <p>MetroCluster was not included in the evaluation and was not tested in the evaluated configuration.</p>
System Manager GUI	The System Manager GUI is considered out of scope and all management is performed via the command line interface.
VMware Virtualization	VMware Virtualization was not included in the evaluation and was not tested in the evaluated configuration.
Cloud environments	ONTAP instances running within a cloud environment were not included in the evaluation and were not tested in the evaluated configuration.

### 3 Documentation

Guidance for configuring a NetApp controller in Common Criteria mode for NetApp Volume Encryption is found in the “NetApp Storage Encryption: Common Criteria Configuration Guide, Version 1.4, November 7, 2024” document.

The following documents, part of the ONTAP 9.14.1 documentation set, may be used in addition to the Common Criteria configuration guide referenced above:

- NetApp Set up, upgrade and revert ONTAP- ONTAP 9, July 02, 2024
- NetApp ONTAP 9.14.1 commands, June 26, 2024

## 4 Security Problem Definition

This security target includes by reference the Security Problem Definition (composed of organizational policies, threat statements, and assumptions) from the [CPP\_FDE\_AA\_V2.0E].

In general, the [CPP\_FDE\_AA\_V2.0E] has presented a Security Problem Definition appropriate for requirements for Data-at-Rest protection for a lost device that contains storage, and as such is applicable to the NetApp Storage Encryption Systems running ONTAP 9.14.1 TOE.

## 5 Security Objectives

The [cPP\_FDE\_AA\_V2.0E] security objectives for the operational environment are reproduced below, these objectives characterize technical and procedural measures each consumer must implement in their operational environment.

In general, the [cPP\_FDE\_AA\_V2.0E] has presented a Security Objectives statement appropriate for Data-at-Rest protection, and as such is applicable to the NetApp Storage Encryption Systems running ONTAP 9.14.1 TOE.

### 5.1 Security Objectives for the Operational Environment

*Table 10: Security Objectives for Operational Environment*

Objective	Description
OE.TRUSTED_CHANNEL	Communication among and between product components (i.e., AA and EE) is sufficiently protected to prevent information disclosure.
OE.INITIAL_DRIVE_STATE	The OE provides a newly provisioned or initialized storage device free of protected data in areas not targeted for encryption.
OE.PASSPHRASE_STRENGTH	An authorized administrator will be responsible for ensuring that the passphrase authorization factor conforms to guidance from the Enterprise using the TOE.
OE.POWER_DOWN	Volatile memory is cleared after power-off so memory remnant attacks are infeasible.
OE.SINGLE_USE_ET	External tokens that contain authorization factors will be used for no other purpose than to store the external token authorization factor.
OE.STRONG_ENVIRONMENT_CRYPTO	The Operating Environment will provide a cryptographic function capability that is commensurate with the requirements and capabilities of the TOE and Appendix A.
OE.TRAINED_USERS	Authorized users will be properly trained and follow all guidance for securing the TOE and authorization factors.
OE.PLATFORM_STATE	The platform in which the storage device resides (or an external storage device is connected) is free of malware that could interfere with the correct operation of the product.
OE.PLATFORM_I&A	The Operational Environment will provide individual user identification and authentication mechanisms that operate independently of the authorization factors used by the TOE.
OE.PHYSICAL	The Operational Environment will provide a secure physical computing space such that an adversary is not able to make modifications to the environment or to the TOE itself.

## 6 IT Security Requirements

This section defines the Security Functional Requirements (SFRs) and Security Assurance Requirements (SARs) that serve to represent the security functional claims for the TOE and to scope the evaluation effort.

The SFRs have all been drawn from the [CPP\_FDE\_AA\_V2.0E].

As a result, any selection, assignment, or refinement operations already performed by that Protection Profile (PP) on the claimed SFRs are not identified here (i.e., they are not formatted in accordance with the conventions specified in section 1.3 of this ST). Formatting conventions are only applied on SFR text that was chosen at the ST author's discretion.

### 6.1 Extended Requirements

All of the extended requirements in this ST have been drawn from the [CPP\_FDE\_AA\_V2.0E]. This ST references the following extended SFRs.

- FCS\_AFA\_EXT.1 Authorization Factor Acquisition
- FCS\_AFA\_EXT.2 Timing of Authorization Factor Acquisition
- FCS\_CKM\_EXT.4(a) Cryptographic Key and Key Material Destruction (Destruction Timing)
- FCS\_CKM\_EXT.4(b) Cryptographic Key and Key Material Destruction (Power Management)
- FCS\_KDF\_EXT.1 Cryptographic Key Derivation
- FCS\_KYC\_EXT.1 Key Chaining (Initiator)
- FCS\_KYC\_EXT.2 Key Chaining (Recipient)
- FCS\_PCC\_EXT.1 Cryptographic Password Construct and Conditioning
- FCS\_RBG\_EXT.1 Cryptographic Operation (Random Bit Generation)
- FCS\_SNI\_EXT.1 Cryptographic Operation (Salt, Nonce, and Initialization Vector Generation)
- FCS\_VAL\_EXT.1 Validation
- FPT\_KYP\_EXT.1 Protection of Key and Key Material
- FPT\_PWR\_EXT.1 Power Saving States
- FPT\_PWR\_EXT.2 Timing of Power Saving States
- FPT\_TST\_EXT.1 TSF Testing
- FPT\_TUD\_EXT.1 Trusted Update

### 6.2 TOE Security Functional Requirements

The following table identifies the SFRs satisfied by the TOE.

Table 11: TOE Security Functional Components

Requirement Class	Requirement Component
<b>FCS: Cryptographic Support</b>	FCS_AFA_EXT.1 Authorization Factor Acquisition
	FCS_AFA_EXT.2 Timing of Authorization Factor Acquisition
	FCS_CKM.1(b): Cryptographic Key Generation (Symmetric Keys)
	FCS_CKM.4(a): Cryptographic Key Destruction (Power Management)
	FCS_CKM.4(d): Cryptographic Key Destruction (Software TOE, 3rd Party Storage)
	FCS_CKM_EXT.4(a) Cryptographic Key and Key Material Destruction (Destruction Timing)
	FCS_CKM_EXT.4(b) Cryptographic Key and Key Material Destruction (Power Management)
	FCS_COP.1(a): Cryptographic Operation (Signature Verification)
	FCS_COP.1(b): Cryptographic Operation (Hash Algorithm)
	FCS_COP.1(c): Cryptographic Operation (Keyed Hash Algorithm)
	FCS_COP.1(d): Cryptographic Operation (Key Wrapping)
	FCS_KDF_EXT.1: Cryptographic Key Derivation
	FCS_KYC_EXT.1: Key Chaining (Initiator)
	FCS_PCC_EXT.1: Cryptographic Password Construct and Conditioning
	FCS_RBG_EXT.1 Cryptographic Operation (Random Bit Generation)
	FCS_SNI_EXT.1: Cryptographic Operation (Salt, Nonce, and Initialization Vector Generation)
FCS_VAL_EXT.1: Validation	
<b>FMT: Security Management</b>	FMT_MOF.1: Management of Functions Behavior
	FMT_SMF.1: Specification of Management Functions
	FMT_SMR.1: Security Roles
<b>FPT: Protection of the TSF</b>	FPT_KYP_EXT.1: Protection of Key and Key Material
	FPT_PWR_EXT.1: Power Saving States
	FPT_PWR_EXT.2: Timing of Power Saving States
	FPT_TST_EXT.1: TSF Testing

Requirement Class	Requirement Component
	FPT_TUD_EXT.1: Trusted Update

## 6.2.1 Cryptographic Support (FCS)

### 6.2.1.1 FCS\_AFA\_EXT.1 Authorization Factor Acquisition

**FCS\_AFA\_EXT.1.1** The TSF shall accept the following authorization factors: [

- ***a submask derived from a password authorization factor conditioned as defined in FCS\_PCC\_EXT.1***

].

### 6.2.1.2 FCS\_AFA\_EXT.2 Timing of Authorization Factor Acquisition

**FCS\_AFA\_EXT.2.1** The TSF shall reacquire the authorization factor(s) specified in FCS\_AFA\_EXT.1 upon transition from any Compliant power saving state specified in FPT\_PWR\_EXT.1 prior to permitting access to plaintext data.

### 6.2.1.3 FCS\_CKM.1(b) Cryptographic Key Generation (Symmetric Keys)

**FCS\_CKM.1.1(b)** The TSF shall generate symmetric cryptographic keys using a Random Bit Generator as specified in FCS\_RBG\_EXT.1 and specified cryptographic key sizes [**256 bit**] that meet the following: [no standard].

### 6.2.1.4 FCS\_CKM.4(a) Cryptographic Key Destruction (Power Management)

**FCS\_CKM.4.1(a)** The TSF shall [**erase**] cryptographic keys and key material from volatile memory when transitioning to a Compliant power saving state as defined by FPT\_PWR\_EXT.1 that meets the following: [a key destruction method specified in FCS\_CKM.4(d)].

### 6.2.1.5 FCS\_CKM.4(d) Cryptographic Key Destruction (Software TOE, 3<sup>rd</sup> Party Storage)

**FCS\_CKM.4.1(d)** The TSF shall destroy cryptographic keys in accordance with a specified cryptographic key destruction method [

- ***For volatile memory, the destruction shall be executed by a [***
  - ***single overwrite consisting of [***
    - ***a pseudo-random pattern using the TSF's RBG,***
    - ***zeroes],***
  - ***removal of power to the memory];***
- ***For non-volatile storage that consists of the invocation of an interface provided by the underlying platform that [***
  - ***logically addresses the storage location of the key and performs a [single] overwrite consisting of [***
    - ***a pseudo-random pattern using the TSF's RBG,***





#### 6.2.1.11 FCS\_COP.1(d) Cryptographic Operation (Key Wrapping)

**FCS\_COP.1.1(d)** The TSF shall perform [key wrapping] in accordance with a specified cryptographic algorithm [AES] in the following modes [*KWP*] and the cryptographic key size [*256 bits*] that meet the following: [AES as specified in ISO/IEC 18033-3, [*NIST SP 800-38F*]].

#### 6.2.1.12 FCS\_KDF\_EXT.1 Cryptographic Key Derivation

**FCS\_KDF\_EXT.1.1** The TSF shall accept [*a conditioned password submask*] to derive an intermediate key, as defined in [

- *NIST SP 800-132*]

using the keyed-hash functions specified in FCS\_COP.1(c), such that the output is at least of equivalent security strength (in number of bits) to the BEV.

#### 6.2.1.13 FCS\_KYC\_EXT.1 Key Chaining (Initiator)

**FCS\_KYC\_EXT.1.1** The TSF shall maintain a key chain of: [

- *intermediate keys originating from one or more submask(s) to the BEV using the following method(s): [*
  - *key derivation as specified in FCS\_KDF\_EXT.1,*
  - *key wrapping as specified in FCS\_COP.1(d)]],*

while maintaining an effective strength of [*256 bits*] for symmetric keys and an effective strength of [*not applicable*] for asymmetric keys.

**FCS\_KYC\_EXT.1.2** The TSF shall provide at least a [*256 bit*] BEV to [encryption engine] [

- *after the TSF has successfully performed the validation process as specified in FCS\_VAL\_EXT.1.*

#### 6.2.1.14 FCS\_PCC\_EXT.1 Cryptographic Password Construct and Conditioning

**FCS\_PCC\_EXT.1.1** A password used by the TSF to generate a password authorization factor shall enable up to [*256*] characters in the set of {upper case characters, lower case characters, numbers, and [*all printable ASCII characters*]} and shall perform Password-based Key Derivation Functions in accordance with a specified cryptographic algorithm HMAC-[*SHA-512*], with [*1024*] iterations, and output cryptographic key sizes [*256 bits*] that meet the following: [NIST SP 800-132].

#### 6.2.1.15 FCS\_RBG\_EXT.1 Cryptographic Operation (Random Bit Generation)

**FCS\_RBG\_EXT.1.1** The TSF shall perform all deterministic random bit generation services in accordance with [*NIST SP 800-90A*] using [*CTR\_DRBG (AES)*].

**FCS\_RBG\_EXT.1.2** The deterministic RBG shall be seeded by at least one entropy source that accumulates entropy from [

- **[2] software-based noise source(s),**
- **[2] hardware-based noise source(s)]**

with a minimum of **[256 bits]** of entropy at least equal to the greatest security strength, according to ISO/IEC 18031:2011 Table C.1 “Security Strength Table for Hash Functions”, of the keys and hashes that it will generate.

#### 6.2.1.16 FCS\_SNI\_EXT.1 Cryptographic Operation (Salt, Nonce, and Initialization Vector Generation)

**FCS\_SNI\_EXT.1.1** The TSF shall **[use salts that are generated by a [DRBG as specified in FCS\_RBG\_EXT.1]]**.

**FCS\_SNI\_EXT.1.2** The TSF shall use **[no nonces]**.

**FCS\_SNI\_EXT.1.3<sup>7</sup>** The TSF shall [use no IVs].

#### 6.2.1.17 FCS\_VAL\_EXT.1 Validation

**FCS\_VAL\_EXT.1.1** The TSF shall perform validation of the **[submask]** using the following method(s): [

- **key wrap as specified in FCS\_COP.1(d)].**

**FCS\_VAL\_EXT.1.2** The TSF shall require validation of the [BEV] prior to [forwarding the BEV to the EE].

**FCS\_VAL\_EXT.1.3** The TSF shall [

- **require power cycle/reset the TOE after [1] of consecutive failed validation attempts]**.

**Application Note:** If an incorrect cluster passphrase is entered at boot, then ONTAP will attempt to boot, but the storage component will not be able to authenticate to the NSE drives. Consequently, ONTAP will not see any drives and will panic, resulting in a system reboot.

While attempting to recover from a failure in the boot media, if an incorrect cluster passphrase is entered at boot, then ONTAP will attempt to boot, but the storage component will not be able to authenticate to the NSE drives. Consequently, ONTAP will not see any drives and will panic, resulting in a system reboot.

---

<sup>7</sup> Modified per TD0760

## 6.2.2 Security Management (FMT)

### 6.2.2.1 FMT\_MOF.1 Management of Functions Behavior

**FMT\_MOF.1.1** The TSF shall restrict the ability to [modify the behaviour of] the functions [use of Compliant power saving state] to [authorized users].

### 6.2.2.2 FMT\_SMF.1 Specification of Management Functions

**FMT\_SMF.1.1**<sup>8</sup> The TSF shall be capable of performing the following management functions: [  
a) forwarding requests to change the DEK to the EE,  
b) forwarding requests to cryptographically erase the DEK to the EE,  
c) allowing authorized users to change authorization values or set of authorization values used within the supported authorization method,  
d) initiate TOE firmware/software updates,  
e) ***[no other functions]***].

### 6.2.2.3 FMT\_SMR.1 Security Roles

**FMT\_SMR.1.1** The TSF shall maintain the roles [authorized user].

**FMT\_SMR.1.2** The TSF shall be able to associate users with roles.

## 6.2.3 Protection of the TSF (FPT)

### 6.2.3.1 FPT\_KYP\_EXT.1 Protection of Key and Key Material

**FPT\_KYP\_EXT.1.1** The TSF shall [  

- ***only store keys in non-volatile memory when wrapped, as specified in FCS\_COP.1(d), or encrypted, as specified in FCS\_COP.1(g) or FCS\_COP.1(e)***].

### 6.2.3.2 FPT\_PWR\_EXT.1 Power Saving States

**FPT\_PWR\_EXT.1.1**<sup>9</sup> The TSF shall define the following Compliant power saving states: [***G2(S5), G3***].

### 6.2.3.3 FPT\_PWR\_EXT.2 Timing of Power Saving States

**FPT\_PWR\_EXT.2.1** For each Compliant power saving state defined in FPT\_PWR\_EXT.1.1, the TSF shall enter the Compliant power saving state when the following conditions occur: user-initiated request, [***shutdown***].

---

<sup>8</sup> Modified per TD0767

<sup>9</sup> Modified per TD0464

#### 6.2.3.4 FPT\_TST\_EXT.1 TSF Testing

**FPT\_TST\_EXT.1.1** The TSF shall run a suite of the following self-tests [*during initial start-up (on power on), at the conditions [before the function is first invoked]*] to demonstrate the correct operation of the TSF: [*cryptographic algorithm tests, software integrity test*].

#### 6.2.3.5 FPT\_TUD\_EXT.1 Trusted Update

**FPT\_TUD\_EXT.1.1** The TSF shall provide [authorized users] the ability to query the current version of the TOE [*software*].

**FPT\_TUD\_EXT.1.2** The TSF shall provide [authorized users] the ability to initiate updates to TOE [*software*].

**FPT\_TUD\_EXT.1.3** The TSF shall verify updates to the TOE software using a [*digital signature as specified in FCS\_COP.1(a)*] by the manufacturer prior to installing those updates.

### 6.3 TOE Security Assurance Requirements

The assurance security requirements for this Security Target are taken from Part 3 of the CC. These assurance requirements compose an Evaluation Assurance Level 1 augmented with the collaborative Protection Profile for Full Drive Encryption – Authorization Acquisition, Version 2.0 + Errata, February 1, 2019. The assurance components are summarized in the following table:

Table 12: Assurance Components

Requirement Class	Requirement Component
<b>Security Target (ASE)</b>	Conformance Claims (ASE_CCL.1)
	Extended Components Definition (ASE_ECD.1)
	ST Introduction (ASE_INT.1)
	Security Objectives for the Operational Environment (ASE_OBJ.1)
	Stated Security Requirements (ASE_REQ.1)
	Security Problem Definition (ASE_SPD.1)
	TOE Summary Specification (ASE_TSS.1)
<b>ADV: Development</b>	Basic Functional Specification (ADV_FSP.1)
<b>AGD: Guidance Documents</b>	Operational User Guidance (AGD_OPE.1)
	Preparative Procedures (AGD_PRE.1)
<b>ALC: Life Cycle Support</b>	Labeling of the TOE (ALC_CMC.1)
	TOE CM Coverage (ALC_CMS.1)
<b>ATE: Tests</b>	Independent Testing – Sample (ATE_IND.1)
<b>AVA: Vulnerability Assessment</b>	Vulnerability Survey (AVA_VAN.1)

**Requirement**

**ASE\_TSS.1.1C** The TOE summary specification shall describe how the TOE meets each SFR, including a proprietary Key Management Description (Appendix E), and **[Entropy Essay, list of all of 3rd party software libraries (including version numbers), 3rd party hardware components (including model/version numbers)]**.

*Table 13: Third Party Components*

Component	NetApp Controller/Drive Model
<b>Third Party Hardware Components tested with TOE<sup>10</sup></b>	
Solid State Drives (SSD/SSD-NVMe)	AFF A150: KPM6WRUG960G (960GB SAS SSD) AFF A320: MZWLJ3T8HBL5-000G6 (3.8TB NVMe) AFF A400: XS960SE70104 (960GB SAS SSD)
Hard Disk Drives	FAS9500: WUS721010AL5205 (10TB SAS HDD)
<b>Third Party Hardware Components available for use with NetApp Controllers</b>	
Solid State Drives (SSD/SSD-NVMe)	TC58NC1132GTC <sup>11</sup> (960GB SAS SSD and 3.8TB SAS SSD) XS3840SE70104 (960GB SAS SSD) MZWLJ3T8HBL5-00AG6 (3.8TB NVMe) XS3840SE70104 (3.8TB SAS SSD) MZWLJ15THALA-00AG6 (15.3TB NVMe)
Hard Disk Drives	ST1800MM0149 (1.8TB SAS HDD) WUS721010AL5205 (10TB SAS HDD)
<b>Third Party Software Components</b>	
OpenSSL	OpenSSL 3.0.8 FIPS Provider
Intel ISA-L Crypto Library	v2.24 Intel Intelligent Storage Acceleration Library Crypto

### 6.3.1 ADV\_FSP.1 Basic functional specification

**ADV\_FSP.1.1D** The developer shall provide a functional specification.

**ADV\_FSP.1.2D** The developer shall provide a tracing from the functional specification to the SFRs.

<sup>10</sup> The drives used in the tested configuration were chosen based on their storage capacity.

<sup>11</sup> The referenced component is a sub-chip that can be embedded into drives with different capacities.

- ADV\_FSP.1.1C** The functional specification shall describe the purpose and method of use for each SFR-enforcing and SFR-supporting TSFI.
- ADV\_FSP.1.2C** The functional specification shall identify all parameters associated with each SFR-enforcing and SFR-supporting TSFI.
- ADV\_FSP.1.3C** The functional specification shall provide rationale for the implicit categorisation of interfaces as SFR-non-interfering.
- ADV\_FSP.1.4C** The tracing shall demonstrate that the SFRs trace to TSFIs in the functional specification.
- ADV\_FSP.1.1E** The evaluator shall confirm that the information provided meets all requirements for content and presentation of evidence.
- ADV\_FSP.1.2E** The evaluator shall determine that the functional specification is an accurate and complete instantiation of the SFRs.

### 6.3.2 AGD\_OPE.1 Operational user guidance

- AGD\_OPE.1.1D** The developer shall provide operational user guidance.
- AGD\_OPE.1.1C** The operational user guidance shall describe, for each user role, the user-accessible functions and privileges that should be controlled in a secure processing environment, including appropriate warnings.
- AGD\_OPE.1.2C** The operational user guidance shall describe, for each user role, how to use the available interfaces provided by the TOE in a secure manner.
- AGD\_OPE.1.3C** The operational user guidance shall describe, for each user role, the available functions and interfaces, in particular all security parameters under the control of the user, indicating secure values as appropriate.
- AGD\_OPE.1.4C** The operational user guidance shall, for each user role, clearly present each type of security-relevant event relative to the user-accessible functions that need to be performed, including changing the security characteristics of entities under the control of the TSF.
- AGD\_OPE.1.5C** The operational user guidance shall identify all possible modes of operation of the TOE (including operation following failure or operational error), their consequences and implications for maintaining secure operation.
- AGD\_OPE.1.6C** The operational user guidance shall, for each user role, describe the security measures to be followed in order to fulfil the security objectives for the operational environment as described in the ST.
- AGD\_OPE.1.7C** The operational user guidance shall be clear and reasonable.

**AGD\_OPE.1.1E** The evaluator shall confirm that the information provided meets all requirements for content and presentation of evidence.

### 6.3.3 AGD\_PRE.1 Preparative procedures

**AGD\_PRE.1.1D** The developer shall provide the TOE including its preparative procedures.

**AGD\_PRE.1.1C** The preparative procedures shall describe all the steps necessary for secure acceptance of the delivered TOE in accordance with the developer's delivery procedures.

**AGD\_PRE.1.2C** The preparative procedures shall describe all the steps necessary for secure installation of the TOE and for the secure preparation of the operational environment in accordance with the security objectives for the operational environment as described in the ST.

**AGD\_PRE.1.1E** The evaluator shall confirm that the information provided meets all requirements for content and presentation of evidence.

**AGD\_PRE.1.2E** The evaluator shall apply the preparative procedures to confirm that the TOE can be prepared securely for operation.

### 6.3.4 ALC\_CMC.1 Labelling of the TOE

**ALC\_CMC.1.1D** The developer shall provide the TOE and a reference for the TOE.

**ALC\_CMC.1.1C** The TOE shall be labelled with its unique reference.

**ALC\_CMC.1.1E** The evaluator shall confirm that the information provided meets all requirements for content and presentation of evidence.

### 6.3.5 ALC\_CMS.1 TOE CM coverage

**ALC\_CMS.1.1D** The developer shall provide a configuration list for the TOE.

**ALC\_CMS.1.1C** The configuration list shall include the following: the TOE itself; and the evaluation evidence required by the SARs.

**ALC\_CMS.1.2C** The configuration list shall uniquely identify the configuration items.

**ALC\_CMS.1.1E** The evaluator shall confirm that the information provided meets all requirements for content and presentation of evidence.

### 6.3.6 ATE\_IND.1 Independent testing - conformance

**ATE\_IND.1.1D** The developer shall provide the TOE for testing.

**ATE\_IND.1.1C** The TOE shall be suitable for testing.



**ATE\_IND.1.1E** The evaluator shall confirm that the information provided meets all requirements for content and presentation of evidence.

**ATE\_IND.1.2E** The evaluator shall test a subset of the TSF to confirm that the TSF operates as specified.

### 6.3.7 AVA\_VAN.1 Vulnerability survey

**AVA\_VAN.1.1D** The developer shall provide the TOE for testing.

**AVA\_VAN.1.1C** The TOE shall be suitable for testing.

**AVA\_VAN.1.1E** The evaluator shall confirm that the information provided meets all requirements for content and presentation of evidence.

**AVA\_VAN.1.2E** The evaluator shall perform a search of public domain sources to identify potential vulnerabilities in the TOE.

**AVA\_VAN.1.3E** The evaluator shall conduct penetration testing, based on the identified potential vulnerabilities, to determine that the TOE is resistant to attacks performed by an attacker possessing Basic attack potential.

## 7 TOE Summary Specification

This Section describes the following security functions:

- Cryptographic Support
- Security Management
- Protection of the TSF

### 7.1 Cryptographic Support

All TOE cryptographic services are provided by the NetApp software modules CryptoMod version 2.2 and the NetApp Cryptographic Security Module (NCSM). NetApp's CryptoMod module is used to:

- Generate salts and keying material via a validated DRBG.
- Derive keys via PBKDFv2.
- Calculate cryptographic hashes.
- Encrypt/decrypt data using validated AES encryption/decryption modes.
- Calculate HMACs.
- Encrypt keys using KWP-AE.
- Decrypt keys using KWP-AD.
- Store volatile keys.

- Zeroize volatile keys.

The NetApp Cryptographic Security Module is used to validate the TOE's cryptographically signed images using approved cryptographic hash and digital signature validation algorithms. All cryptographic algorithms are NIST CAVP certified. The following table identifies the cryptographic algorithms used by the TSF, the associated standards to which they conform, and the NIST certificates that demonstrate that the claimed conformance has been met.

Table 14: *CryptoMod version 2.2 Algorithm Certificates*

SFR	Algorithm	Standard	Certificate
Cryptographic Operation (Hash Algorithm)			
FCS_COP.1.1(b)	SHA-256, SHA-512	ISO/IEC 10118-3:2004	A4794, A4795: SHA2-256  A4794, A4795: SHA2-512
Cryptographic Operation (Keyed Hash Algorithm)			
FCS_COP.1.1(c)	HMAC-512	ISO/IEC 9797-2:2011, Section 7 "MAC Algorithm 2"	A4794, A4795: HMAC-SHA2-512
Cryptographic Operation (Key Wrapping)			
FCS_COP.1.1(d)	AES-KWP-256	NIST SP 800-38F	A4794, A4795: AES-KWP
Cryptographic Operation (Random Bit Generation)			
FCS_RBG_EXT.1	AES-256 (CTR_DRBG)	NIST SP 800-90A	A4794, A4795: Counter DRBG

Table 15: *NetApp Cryptographic Security Module (NCSM v3.0.8) Algorithm Certificates*

SFR	Algorithm	Standard	Certificate
Cryptographic Operation (Signature Verification)			
FCS_COP.1.1(a)	RSA 3072	FIPS PUB 186-4, "Digital Signature Standard (DSS)", Section 5.5, using PKCS #1 v2.1 Signature Schemes RSASSA-PSS and/or RSASSA-PKCS1-v1_5; ISO/IEC 9796-2, Digital signature scheme 2 or	A4858: RSA SigVer (FIPS 186-4)

SFR	Algorithm	Standard	Certificate
		Digital Signature scheme 3, for RSA schemes	
Cryptographic Operation (Hash Algorithm)			
FCS_COP.1.1(b)	SHA-256 SHA-384 SHA-512	ISO/IEC 10118-3:2004	A4858: SHA2-256 A4858: SHA2-384 A4858: SHA2-512

### 7.1.1 FCS\_AFA\_EXT.1: Authorization Factor Acquisition

The cluster passphrase serves as the password authorization factor. In CC mode, the cluster passphrase (64-256 bytes) must be entered at boot before ONTAP is allowed to boot. The Cluster Passphrase Key Encryption Key (CP-KEK) is derived from the Cluster Passphrase (CP) and the Cluster Salt using a NIST SP 800-132 approved password-based key derivation function (PBKDFv2).

There are three operations that require a user to enter the existing cluster passphrase:

1. Whenever the cluster passphrase is changed (security key-manager onboard update-passphrase).
2. When booting ONTAP (CC mode only).
3. When recovering from a failure in the boot media.

### 7.1.2 FCS\_AFA\_EXT.2: Timing of Authorization Factor Acquisition

The TOE provides the Compliant power saving states of G3 (mechanical off) and G2(S5) (soft off). The Compliant power saving states require that the cluster passphrase be entered at boot before ONTAP is allowed to boot.

### 7.1.3 FCS\_CKM.1(b): Cryptographic Key Generation (Symmetric Keys)

The TOE generates the Cluster Key Encryption Key (CKEK) which is a 256-bit AES key generated by the CryptoMod DRBG. The wCKEK is the encrypted (wrapped) form of CKEK using [NIST 800-38F] KWP-AE with CP-KEK used as the encrypting (wrapping) key. The CKEK key is used to wrap the Authentication Keys (AKs).

ONTAP creates two 32-byte DRBG generated authentication keys (collectively known as AKs) when the ONTAP Onboard Key Manager is configured. The two instances of authentication keys are called AK1 and AK2. Customers are free to assign AK1 (or AK2) to either the data or FIPS port of the controller's NSE drive. Best practices dictate that one AK be used for the drive's data port and the other for the drive's FIPS port.

Neither AK1 nor AK2 is persistently stored. The wrapped versions of the AKs (wAK1 and wAK2) are persistently stored in the OKM DB. For FDE-AA with NSE, the AK is a BEV.

### 7.1.4 FCS\_CKM.4(a): Cryptographic Key Destruction (Power Management)

The TOE provides the Compliant power saving states of G3 (mechanical off) and G2(S5) (soft off).

The keys in volatile memory are destroyed when entering the G3 or G2(S5) Compliant power saving state. In both states, power is removed from memory and all values drain to a zero state.

The G2(S5) Compliant power saving state is entered when an authorized user executes the `system node halt` command.

The G3 Compliant power saving state is entered when power to the appliance is removed, for example, by throwing the on/off switch or pulling the power plug.

Once either of the two Compliant power saving states is entered, the cluster passphrase must be entered in order for ONTAP to boot and resume operation.

### 7.1.5 FCS\_CKM.4(d): Cryptographic Key Destruction (Software TOE, 3<sup>rd</sup> Party Storage)

Both AK1 and AK2 remain active as long as OKM is configured. New versions of AK1 and AK2 can be generated by first disabling OKM (`security key-manager onboard disable`) and then re-enabling OKM (`security key-manager onboard enable`). When OKM is disabled, all keys associated with OKM (CP-KEK, CKEK, AK1, and AK2) will be destroyed.

When a key is destroyed (or zeroized), it is destroyed in the following manner:

- Key stored in non-volatile memory:
  - Storage location overwritten with DRBG generated random data.
  - Storage location overwritten with zeroes.
  - Storage location read and compared with zero.
- Key stored in volatile memory:
  - Memory location overwritten with DRBG generated random data.
  - Memory location overwritten with zeroes.
  - Memory location read and compared with zero.

The CP, CP-KEK, CKEK, AK1, and AK2 will be destroyed by the removal of power to the memory when entering the G3 and G2(S5) Compliant power saving states.

The following table summarizes the CSPs and keys used by the TOE. The keys are identified as which are stored in volatile memory and non-volatile memory.

*Table 16: Critical Security Parameters*

Name	Use	Type	Source	Storage within Volatile Memory	Storage within non-volatile Memory	Notes
CP	Authorization factor	64 to 256-byte ASCII passphrase	Supplied by cluster administrator when "security key-manager onboard enable" is executed, when "security key-manager onboard update-passphrase" is executed and when ONTAP is rebooted.	Temporarily "stored" as a variable or a CPU register value in functions involved in the calculation of the CP-KEK.  Not stored in the cryptomod key table.	Not stored in non-volatile memory.	1
CP-Salt	Salt used with CP when creating CP Hash	64-byte random salt	Cryptomod DRBG	Temporarily "stored" as a variable or CPU register value in functions involved in the calculation of the CP-KEK or functions that need to store the CP-Salt in non-volatile memory.  Not stored in the cryptomod key table.	Persistently stored in non-volatile memory in an RDB table and the OKM database.	2
CP-Hash	Used to validate the CP	SHA-256(CP-Salt    CP)	Cryptomod SHA-256 performed on the CP	Temporarily "stored" as a variable or CPU register value in functions that need to validate the cluster passphrase, or functions that need to store the CP-Hash in	Persistently stored in non-volatile memory within an RDB table.	2

Name	Use	Type	Source	Storage within Volatile Memory	Storage within non-volatile Memory	Notes
				non-volatile memory.  Not stored in the cryptomod key table.		
Cluster-Salt	Salt used in creation of CP-KEK	64-byte random salt	Cryptomod DRBG	Temporarily “stored” as a variable or CPU register value in functions that need to calculate the CP-KEK, or functions that need to store the Cluster-Salt in non-volatile memory.  Not stored in the cryptomod key table.	Persistently stored in non-volatile memory within an RDB table and the OKM DB.	2,3
CP-KEK	Used to protect the CKEK.	AES-256 key derived using PBKDF2 function with HMAC-SHA-512 used as the PRF (1024 iterations).	Cryptomod PBKDF2	Temporarily “stored” as a variable or CPU register value in functions that need to wrap/unwrap the CKEK.  Stored in cryptomod’s key table.	Not stored in non-volatile memory.	4
CKEK	Used to protect each of the SVM-KEKs.	AES-256 key	Cryptomod DRBG	Temporarily “stored” as a variable or CPU register value in functions that need to wrap/unwrap	Not stored in non-volatile memory.	4

Name	Use	Type	Source	Storage within Volatile Memory	Storage within non-volatile Memory	Notes
				<p>keys wrapped by the CKEK.</p> <p>Stored in cryptomod's key table.</p>		
wCKEK	Encrypted form of CKEK	Wrapped (encrypted) form of CKEK using [NIST 800-38F] KWP-AE with CP-KEK used as the encrypting key	Cryptomod KWP	<p>Temporarily "stored" as a variable or CPU register value in functions that need to calculate the wCKEK, functions that need to unwrap the wCKEK, or functions that need to store the wCKEK in non-volatile memory.</p> <p>Not stored in the cryptomod key table.</p>	Persistently stored in non-volatile memory within an RDB table and the OKM DB.	2,3
AK	Authentication Key	32-byte random value	Cryptomod DRBG	Temporarily "stored" as a variable or CPU register value in functions that need to calculate the wAK and functions that need to use the AK to authenticate to a SED.	Not stored in non-volatile memory	5

Name	Use	Type	Source	Storage within Volatile Memory	Storage within non-volatile Memory	Notes
				Stored in the cryptomod key table.		
wAK	Encrypted form of AK	Wrapped (encrypted) form of AK using [NIST 800-38F] KWP-AE with CKEK used as the encrypting key	Cryptomod KWP	Temporarily “stored” as a variable or CPU register value in functions that need to calculate the wAK, functions that need to unwrap the wAK, and functions that need to store the wAK to non-volatile memory.  Not stored in the cryptomod key table.	Persistently stored in non-volatile memory within an RDB table and the OKM DB.	2,3,6

**Notes:**

1. The cluster passphrase (CP) is required when setting up OKM, when updating the passphrase, when ONTAP reboots, and when performing a recovery operation.
2. RDB is the replicated, cluster-wide database used by ONTAP.
3. The OKM DB file, located at `/cfc card/kmip/km_onboard.wkeydb`, contains the cluster salt, the node salt (not used in Common Criteria mode), encrypted key material (wCKEK, wAK1, and wAK2), and key IDs.
4. The cryptomod key table memory utilizes memory that is auto zeroed during core dumps.
5. There are two AKs generated: AK1 and AK2. AK is used to generically refer to both instances.
6. There are two wAKs generated: wAK1 and wAK2. wAK is used to generically refer to both instances.

The TOE temporarily stores key material such as the unwrapped AK and the PBKDFv2 based upon the Cluster Passphrase and salt. The TOE clears the keys from memory by removal of power. The TOE also clears the keys when they are no longer needed.

The following keys are destroyed when the Cluster Passphrase is changed or the OKM is deleted:



- CP-KEK
- CKEK
- wCKEK
- AK (AK1 and AK2)
- wAK (wAK1 and wAK2).

The OKM is deleted on the following conditions:

- No further need for a key manager
- Migration to an external configuration.

The OKM cannot be deleted if any NSE drive within the ONTAP cluster is locked with either AK1 or AK2.

#### 7.1.6 FCS\_CKM\_EXT.4(a) Cryptographic Key and Key Material Destruction (Destruction Timing)

The TOE shall delete all keys and key material when no longer needed. The keys are destroyed when the OKM is deleted. The OKM DB is deleted when the Onboard Key Manager is deleted.

Neither the AK1 nor AK2 is persistently stored. The wrapped versions of the AKs (wAK1 and wAK2) are persistently stored in the OKM DB. The internal Onboard Key Manager (OKM) is used to manage the authentication key (AK) used by the array's SED capable drives.

#### 7.1.7 FCS\_CKM\_EXT.4(b): Cryptographic Key and Key Material Destruction (Power Management)

The CP, CP-KEK, CKEK, AK1, and AK2 will be destroyed by the removal of power to the memory when entering the G3 and G2(S5) Compliant power saving states.

An authorized user can execute the `system node halt` command for the G2(S5) Compliant power saving state. The TOE must be fully rebooted from each Compliant power saving state.

#### 7.1.8 FCS\_COP.1(a): Cryptographic Operation (Signature Verification)

The TOE implements the RSA Digital Signature Algorithm with a key size (modulus) of 3072-bits with SHA-384 signatures to verify authenticity of the trusted updates. Upon receiving an update and the signature file, the TOE uses the embedded public key stored in the firmware on the NetApp Appliance. The TOE will verify the signature before installing it and reject any update with an invalid signature.

#### 7.1.9 FCS\_COP.1(b): Cryptographic Operation (Hash Algorithm)

The TOE performs SHA-256, SHA-384 and SHA-512 cryptographic hashing services that meet the following: ISO/IEC 10118-3:2004. The TOE uses the SHA-384 hash functions as part of the RSA signature verification function for the trusted updates. The TOE uses the SHA-512 hash function as part of the PBKDFv2 to produce the cluster passphrase. A SHA-256 digest is used to validate the cluster passphrase (CP).

### 7.1.10 FCS\_COP.1(c): Cryptographic Operation (Keyed Hash Algorithm)

The TOE performs HMAC-SHA-512 message authentication using cryptographic key sizes 512-2048 bits that meet the following: ISO/IEC 9797-2:2011, Section 7 "MAC Algorithm 2". HMAC-SHA-512 is used for PBKDF2. In PBKDF2, the OKM passphrase is used as the HMAC-SHA-512 key.

Table 17: HMAC Details

HMAC Function	Key Length	Block Size	Output MAC Length	Hash Function Used
HMAC-SHA-512	L2 = 512, L1 = 2048	1024	512	SHA-512

### 7.1.11 FCS\_COP.1(d): Cryptographic Operation (Key Wrapping)

The TOE uses the NIST 800-38F KWP-AE(P) routine to encrypt (or "wrap") a key and the corresponding NIST 800-38F KWP-AD(C) routine to decrypt ("unwrap") an encrypted, or wrapped, key.

### 7.1.12 FCS\_KDF\_EXT.1: Cryptographic Key Derivation

The TOE implements PBKDFv2 with HMAC-SHA-512, 1024 iterations, and a salt value of 512 bits to transform the Cluster Passphrase into a derived key as specified in SP800-132; the CP-KEK is used to unwrap the wrapped CKEK, which in turn is used to unwrap the wrapped AKs, which in turn are passed to the EE as BEVs. The output is at least of equivalent security strength (in number of bits) to the BEV.

### 7.1.13 FCS\_KYC\_EXT.1: Key Chaining (initiator)

The TOE uses the PBKDFv2 key derivation function to transform the Cluster Passphrase into the CP-KEK, which is used to unwrap the wCKEK, which is then used to unwrap the wAK, which then constitutes the BEV. The Cluster Passphrase is validated by hashing it along with the CP Salt and comparing the result to the persistently stored CP Hash. If the two values are equal, the Cluster Passphrase is correct and so it can be used to derive the CP-KEK.

### 7.1.14 FCS\_PCC\_EXT.1: Cryptographic Password Construct and Conditioning

The TOE accepts passwords up to 256 characters. The character set can consist of all upper-case characters, lower-case characters, numbers, and all printable ASCII characters. The password is conditioned using PBKDFv2 that meets SP800-132. The cryptographic algorithm implements HMAC-SHA-512, a salt value of 512 bits from the DRBG, and 1024 iterations to produce a cryptographic key size of 256-bits.

### 7.1.15 FCS\_RBG\_EXT.1 Cryptographic Operation (Random Bit Generation)

Random bits are produced by a DRBG implemented within the NetApp CryptoMod module. The DRBG uses the CTR\_DRBG algorithm from NIST SP 800-90A. The implementation of CTR\_DRBG uses AES-256 (maximum of 256 bits of security strength) as the block cipher along with the appropriate derivation

function. The CTR\_DRBG (AES) is seeded with 384-bits from the OS's CSPRNG (Fortuna) via the /dev/random interface. The Fortuna CSPRNG noise sources are provided by:

- two hardware-based noise sources
  - Intel RDSEED instruction (primary noise source)
  - Ethernet interrupts
- two software-based noise sources
  - Software interrupts
  - Event driven interrupts

#### 7.1.16 FCS\_SNI\_EXT.1 Cryptographic Operation (Salt, Nonce, and Initialization Vector Generation)

The TOE generates salts using its CTR\_DRBG (AES) to generate the cluster salt, a 64-byte random number. The cluster salt is used along with the cluster passphrase to derive the cluster passphrase key encryption key (CP-KEK) using a NIST SP 800-132 approved PBKDFv2 algorithm.

The TOE does not implement any of these modes of AES: (CBC, CCM, XTS, or GCM), so this requirement is vacuously satisfied.

#### 7.1.17 FCS\_VAL\_EXT.1 Validation

The Cluster Passphrase is a 64–256-byte customer generated ASCII string that is used as an authorization factor. The Cluster Passphrase is used in conjunction with a salt value to derive a cluster passphrase key encryption key (CP-KEK) via a NIST SP 800-132 ([NIST 800-132]) approved PBKDFv2 algorithm. The CP-Hash is used by OKM when there is a need to validate that the Cluster Passphrase has been entered correctly by a storage administrator. As indicated in Table 10, the CP-Hash is defined as SHA-256(CP-Salt || Cluster Passphrase) where '||' denotes concatenation.

During booting ONTAP or recovering from a failure in the boot media, if an incorrect cluster passphrase is entered at boot, then ONTAP will attempt to boot, but the storage component will not be able to authenticate to the NSE drives. As a consequence, ONTAP will not see any drives and will panic, resulting in a system reboot. The only way to recover is to manually reboot and enter the correct passphrase.

When modifying the cluster passphrase, a customer is allowed 5 consecutive failed attempts to authenticate with the current cluster passphrase. After 5 consecutive failed attempts, the cluster passphrase may be modified only by (a) rebooting one or more nodes within the cluster, or (b) waiting for 24 hours to elapse before re-attempting to modify the cluster passphrase.

There are three operations that require a user to enter the existing cluster passphrase:

1. Changing the cluster passphrase (security key-manager onboard update-passphrase);
2. Booting ONTAP (CC mode only);

### 3. Recovering from a failure in the boot media.

The ONTAP limit on the number of failed authentication attempts for each of the scenarios listed above, as well as a description of how ONTAP behaves when the limit is reached, is provided in the following table.

*Table 18: Maximum Failed Attempts*

Scenario	Maximum Failed Authentication Attempt Limit	ONTAP Behavior When Limit Reached
Changing Cluster Passphrase	5	When the maximum is exceeded, either one (or more) nodes within the cluster must be successfully rebooted or a 24-hour time period needs to elapse before the administrator is allowed to change the cluster passphrase.
Booting ONTAP	1	If an incorrect cluster passphrase is entered at boot, then ONTAP will attempt to boot, but the storage component will not be able to authenticate to the NSE drives. As a consequence, ONTAP will not see any drives and will panic, resulting in a system reboot.
Recovering from a failure in the boot media	1	If an incorrect cluster passphrase is entered at boot, then ONTAP will attempt to boot, but the storage component will not be able to authenticate to the NSE drives. As a consequence, ONTAP will not see any drives and will panic, resulting in a system reboot.

## 7.2 Security Management

### 7.2.1 FMT\_MOF.1 Management of Functions Behavior

The TOE provides the Compliant power saving states of G3 (mechanical off) and G2(S5) (soft off).

The TOE enters the G3 (mechanical off) state when the administrator removes the device power via a mechanical switch. Only an authorized user can execute the `system node halt` command for the G2(S5) Compliant power saving state.

### 7.2.2 FMT\_SMF.1: Specification of Management Functions

The TOE is capable of performing the following management functions:

- Forwarding requests to change the DEK to the EE

The TOE can send a request to the drive to change the DEK via the `storage encryption disk sanitize` CLI command.

- Forwarding requests to cryptographically erase the DEK to the EE

The TOE can send a request to cryptographically erase the disk, followed by changing the AK to random value that is not retained by ONTAP using the `storage encryption disk destroy` CLI command.

- Allowing authorized users to change authorization factors or set of authorization factors used

Authorized users can change authorization factors or set of authorization factors used by the `security key-manager onboard update-passphrase` command.

- Initiate TOE firmware/software updates

The authorized user can initiate TOE software updates via the `cluster image update` CLI command.

### 7.2.3 FMT\_SMR.1: Security Roles

The TOE maintains the role of authorized user.

## 7.3 Protection of the TSF

### 7.3.1 FPT\_KYP\_EXT.1: Protection of Key and Key Material

The TOE stores the following encrypted keys in the Onboard Key Manager (OKM):

- wAK – (Encrypted form of AK) - wrapped (encrypted) form of AK using [NIST 800-38F] KWP-AE with CKEK used as the encrypting key,
- wCKEK – (Encrypted form of CKEK) - Wrapped (encrypted) form of CKEK using [NIST 800-38F] KWP-AE with CP-KEK used as the encrypting key.

### 7.3.2 FPT\_PWR\_EXT.1: Power Saving States / FPT\_PWR\_EXT.2: Timing of Power Saving States

The TOE enters the G3 (mechanical off) state when the administrator removes the device power via a mechanical switch. An authorized user can execute the `system node halt` command for the G2(S5) Compliant power saving state. The TOE must be fully rebooted from each Compliant power saving state.

### 7.3.3 FPT\_TST\_EXT.1: TSF Testing

The TOE includes power-on self-tests to ensure that the TOE is operating correctly. The power-on self-tests include a Known Answer Test (KAT) to verify the correctness of the cryptographic algorithms and the software integrity test to ensure that the module is not corrupted.

The KATs for cryptographic functions consist of executing each function on data for which the correct answer is already known. The output produced by the tested function is compared with the known answer. If they are not identical, the KAT fails.

The TOE includes the following power-on self-tests to ensure that the cryptographic functionality is performing correctly:

The Known Answer Tests (KATs) include the following:

- AES-128 CBC, AES-256 CBC – encryption/decryption test
- 256-AES-KWP – encryption/decryption test
- DRBG – Tested per SP800-90A, including the Health Testing identified in Section 11.3.
- HMAC SHA-512 - keyed-hash message authentication code test
- PBKDF2 - Password-Based Key Derivation Function 2 test
- SHA-256, SHA-384, SHA-512 - hashing test
- RSA Signature Generation/Verification – 3072-bits

The TOE performs the following software integrity test to ensure that the module is not corrupted.

- Software integrity test - During power-on self-testing, the module performs a self-integrity check and compares the results against the build time generated hash digests. During power on, the bootloader validates the whitelist database of secure boot keys with the signature associated with each module that is loaded. After each module is validated and loaded, the boot process continues with the ONTAP initialization. If signature validation fails for any module, the system reboots.

#### 7.3.4 FPT\_TUD\_EXT.1: Trusted Update

The TSF provides authorized users the ability to query the current version of the TOE. The administrator executes the `cluster image show` or the `version` command to display the current version of the TOE.

NetApp code signing ensures that ONTAP images installed through non-disruptive image updates or automated non-disruptive image updates are authentically produced by NetApp and have not been tampered with. The NetApp updates are cryptographically signed using an RSA Digital Signature algorithm with a key size of 3072-bits with a SHA-384 signature. The private keys, used for code signing, are stored in a limited access HSM at NetApp. If the TOE's public keys are tampered with then an update will fail.

The TOE will verify the signature before installing the update and reject any update with an invalid signature.

This is a no-touch security feature for upgrading ONTAP versions. The user is not expected to do anything differently except for optionally verifying the top-level image.tgz signature.

## 8 Protection Profile Claims

The ST conforms to:

- collaborative Protection Profile for Full Drive Encryption – Authorization Acquisition, Version 2.0 + Errata, February 1, 2019 [cPP\_FDE\_AA\_V2.0E] with the following optional and selection-based SFRs: FCS\_CKM.1(b), FCS\_COP.1(a), FCS\_COP.1(b), FCS\_COP.1(c), FCS\_COP.1(d), FCS\_KDF\_EXT.1, FCS\_PCC\_EXT.1, FCS\_RBG\_EXT.1, FCS\_VAL\_EXT.1, FPT\_TST\_EXT.1.

As explained in Section 3, the Security Problem Definition of the [cPP\_FDE\_AA\_V2.0E] has been included by reference into this ST.

As explained in Section 5, Security Objectives, the Security Objectives of the [cPP\_FDE\_AA\_V2.0E] has been included by reference into this ST.

The following table identifies all the Security Functional Requirements (SFRs) in this ST. Each SFR is reproduced from the [cPP\_FDE\_AA\_V2.0E] and operations completed as appropriate.

Table 19: Security Functional Requirements

Requirement Class	Requirement Component	Source
<b>FCS: Cryptographic Support</b>	FCS_AFA_EXT.1 Authorization Factor Acquisition	[cPP_FDE_AA_V2.0E]
	FCS_AFA_EXT.2 Timing of Authorization Factor Acquisition	[cPP_FDE_AA_V2.0E]
	FCS_CKM.1(b): Cryptographic Key Generation (Symmetric Keys)	[cPP_FDE_AA_V2.0E]
	FCS_CKM.4(a): Cryptographic Key Destruction (Power Management)	[cPP_FDE_AA_V2.0E]
	FCS_CKM.4(d): Cryptographic Key Destruction (Software TOE, 3rd Party Storage)	[cPP_FDE_AA_V2.0E]
	FCS_CKM_EXT.4(a) Cryptographic Key and Key Material Destruction (Destruction Timing)	[cPP_FDE_AA_V2.0E]
	FCS_CKM_EXT.4(b) Cryptographic Key and Key Material Destruction (Power Management)	[cPP_FDE_AA_V2.0E]
	FCS_COP.1(a): Cryptographic Operation (Signature Verification)	[cPP_FDE_AA_V2.0E]
	FCS_COP.1(b): Cryptographic Operation (Hash Algorithm)	[cPP_FDE_AA_V2.0E]
	FCS_COP.1(c): Cryptographic Operation (Keyed Hash Algorithm)	[cPP_FDE_AA_V2.0E]
	FCS_COP.1(d): Cryptographic Operation (Key Wrapping)	[cPP_FDE_AA_V2.0E]
	FCS_KDF_EXT.1: Cryptographic Key Derivation	[cPP_FDE_AA_V2.0E]
	FCS_KYC_EXT.1: Key Chaining (Initiator)	[cPP_FDE_AA_V2.0E]

Requirement Class	Requirement Component	Source
	FCS_PCC_EXT.1: Cryptographic Password Construct and Conditioning	[cPP_FDE_AA_V2.0E]
	FCS_RBG_EXT.1: Cryptographic Operation (Random Bit Generation)	[cPP_FDE_AA_V2.0E]
	FCS_SNI_EXT.1: Cryptographic Operation (Salt, Nonce, and Initialization Vector Generation)	[cPP_FDE_AA_V2.0E]
	FCS_VAL_EXT.1: Validation	[cPP_FDE_AA_V2.0E]
<b>FMT: Security Management</b>	FMT_MOF.1: Management of Functions Behavior	[cPP_FDE_AA_V2.0E]
	FMT_SMF.1: Specification of Management Functions	[cPP_FDE_AA_V2.0E]
	FMT_SMR.1: Security Roles	[cPP_FDE_AA_V2.0E]
<b>FPT: Protection of the TSF</b>	FPT_KYP_EXT.1: Protection of Key and Key Material	[cPP_FDE_AA_V2.0E]
	FPT_PWR_EXT.1: Power Saving States	[cPP_FDE_AA_V2.0E]
	FPT_PWR_EXT.2: Timing of Power Saving States	[cPP_FDE_AA_V2.0E]
	FPT_TST_EXT.1: TSF Testing	[cPP_FDE_AA_V2.0E]
	FPT_TUD_EXT.1: Trusted Update	[cPP_FDE_AA_V2.0E]

## 9 Rationale

This security target includes by reference the [cPP\_FDE\_AA\_V2.0E] Security Problem Definition, Security Objectives, and Security Assurance Requirements. The security target makes no additions to the [cPP\_FDE\_AA\_V2.0E] assumptions. [cPP\_FDE\_AA\_V2.0E] and security functional requirements have been reproduced with the Protection Profile operations completed. Operations on the security requirements follow [cPP\_FDE\_AA\_V2.0E] application notes and assurance activities. Consequently, [cPP\_FDE\_AA\_V2.0E] rationale applies but is incomplete. The TOE Summary Specification rationale below serves to complete the rationale required for the security target.

### 9.1 TOE Summary Specification Rationale

Each subsection in Section 7, the TOE Summary Specification, describes a security function of the TOE. Each description is followed with rationale that indicates which requirements are satisfied by aspects of the corresponding security function. The security functions work together to satisfy all of the security functional requirements. Furthermore, all of the security functions are necessary in order for the TSF to provide the required security functionality.

This Section in conjunction with Section 7, the TOE Summary Specification, provides evidence that the security functions are suitable to meet the TOE security requirements. The collection of security functions work together to provide all of the security requirements. The security functions described in the TOE summary specification are all necessary for the required security functionality in the TSF. Table 20



“Security Functions vs. Requirements Mapping” demonstrates the relationship between security requirements and security functions.

Table 20: Security Functions vs. Requirements Mapping

Specification	Cryptographic support	Security management	Protection of the TSF
FCS_AFA_EXT.1	X		
FCS_AFA_EXT.2	X		
FCS_CKM.1(b)	X		
FCS_CKM.4 (a)	X		
FCS_CKM.4 (d)	X		
FCS_CKM_EXT.4 (a)	X		
FCS_CKM_EXT.4 (b)	X		
FCS_COP.1(a)	X		
FCS_COP.1(b)	X		
FCS_COP.1(c)	X		
FCS_COP.1(d)	X		
FCS_KDF_EXT.1	X		
FCS_KYC_EXT.1	X		
FCS_KYC_EXT.2	X		
FCS_PCC_EXT.1	X		
FCS_RBG_EXT.1	X		
FCS_SNI_EXT.1	X		
FCS_VAL_EXT.1	X		
FMT_MOF.1		X	
FMT_SMF.1		X	
FMT_SMR.1		X	
FPT_KYP_EXT.1			X
FPT_PWR_EXT.1			X
FPT_PWR_EXT.2			X
FPT_TUD_EXT.1			X
FPT_TST_EXT.1			X